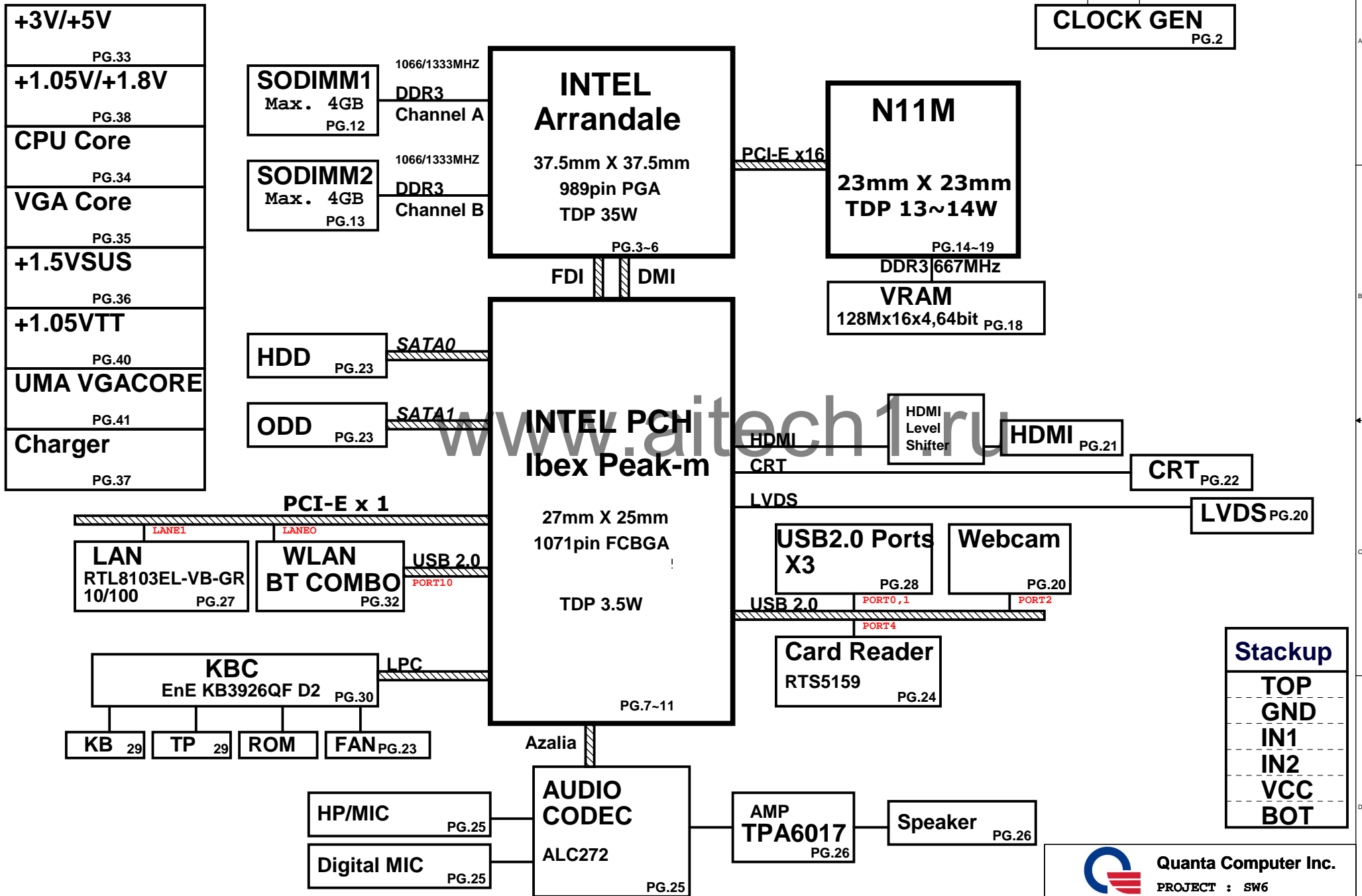
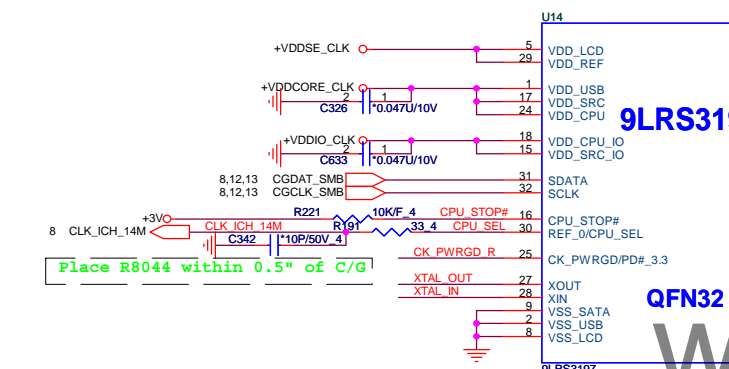
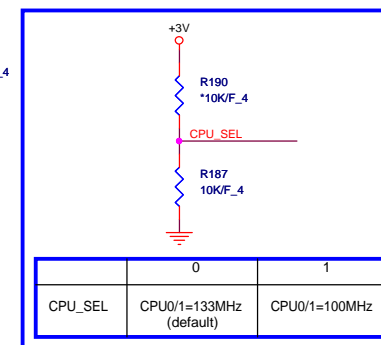
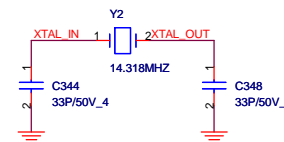
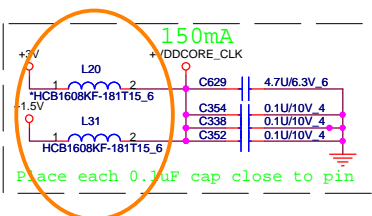
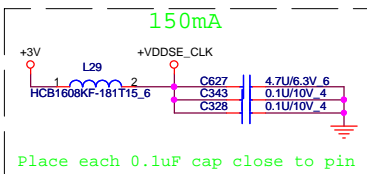
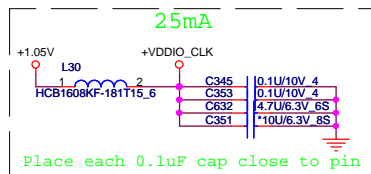
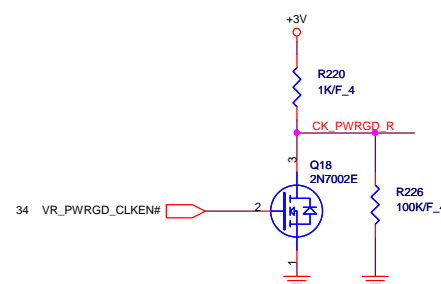


SW6 INTEL OPTIMUS SYSTEM DIAGRAM

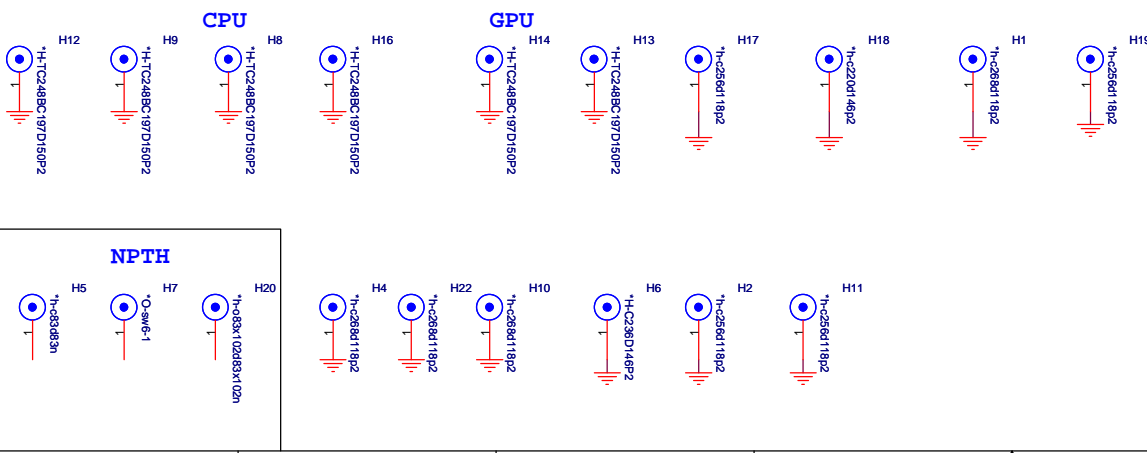
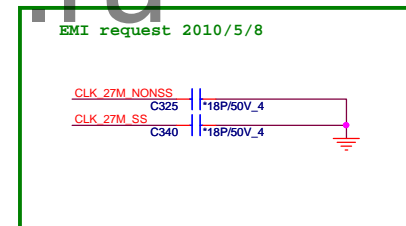
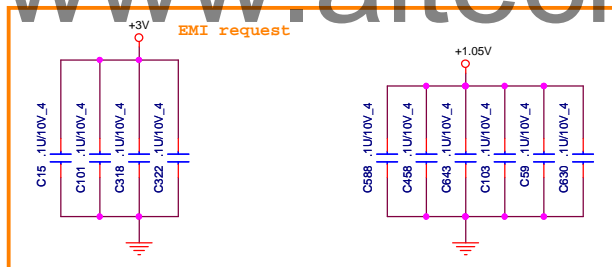




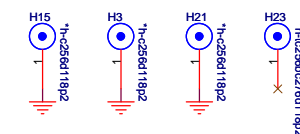
www.aitech1.ru




1.05V 7,8,9,11,34,35,38,41
1.5V 32,39
+3V 3,7,8,9,10,11,12,13,19,20,21,22,23,24,25,26,27,29,32,34,39



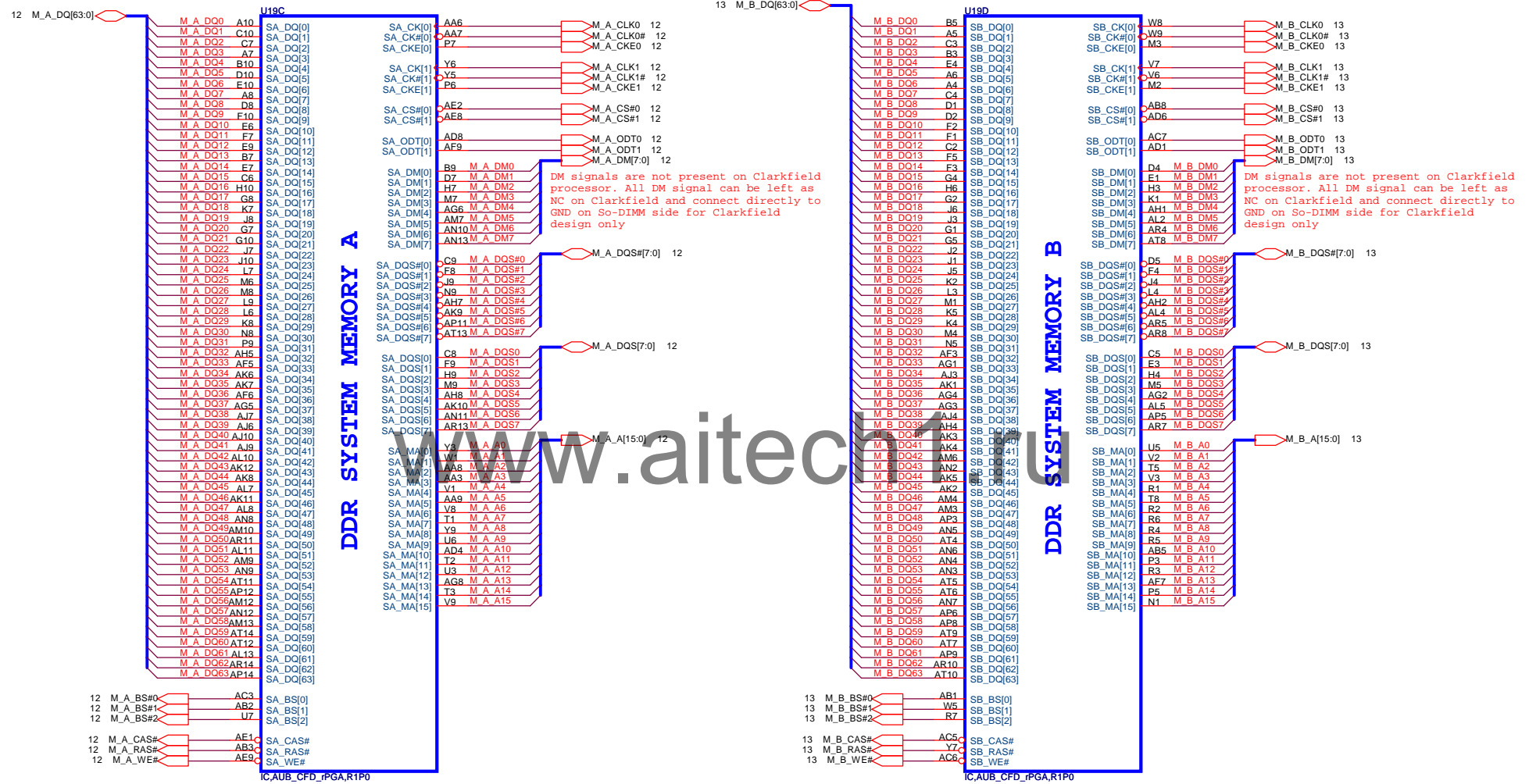
For ESD require to keep floating for A version





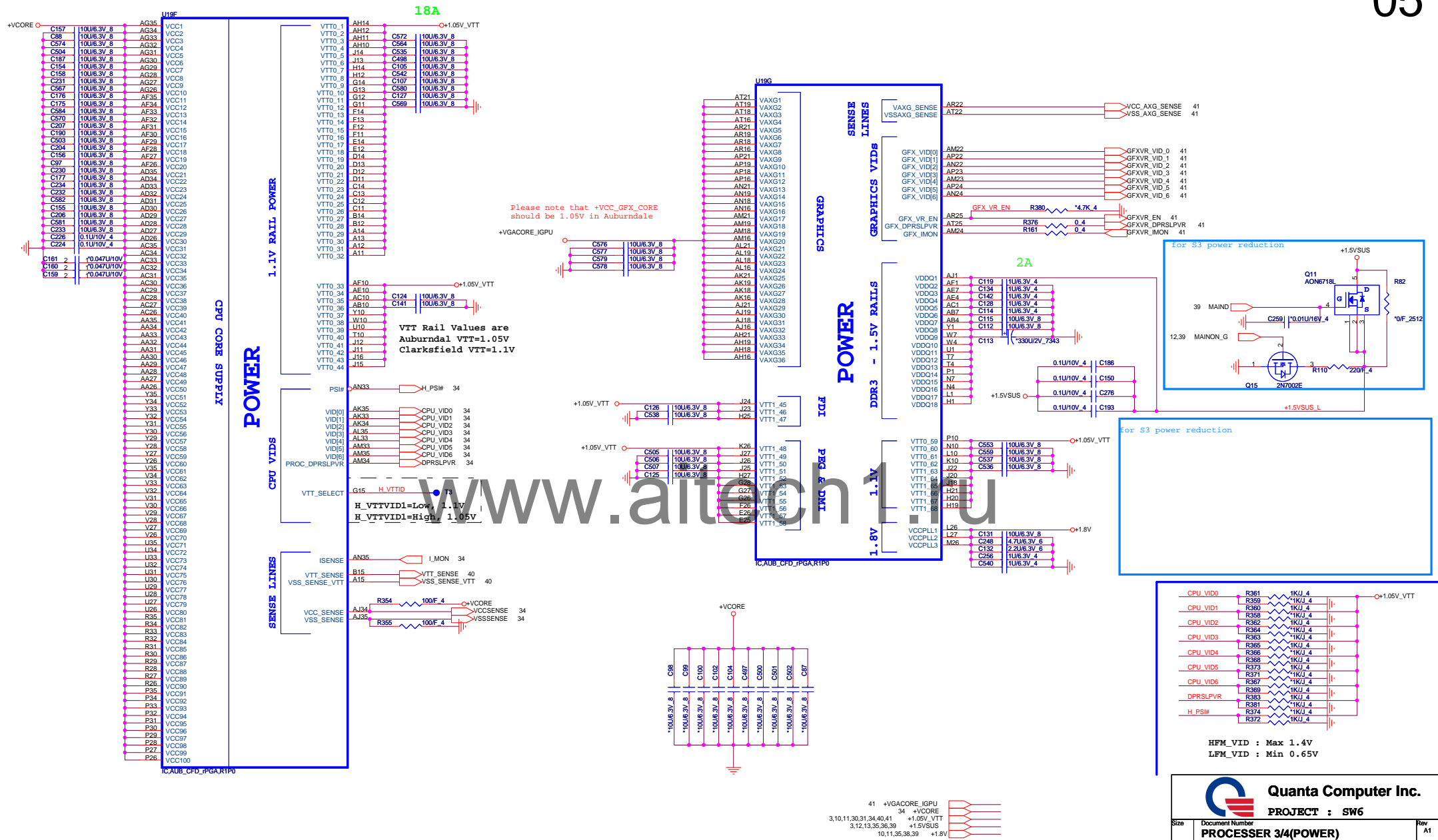
 Quanta Computer Inc. PROJECT : SW6	
Size	Document Number PROCESSOR 1/4(HOST&PEX)
Date: Tuesday, May 25, 2010	Sheet 3 of 42 Rev A1

AUBURNDALE/CLARKSFIELD PROCESSOR (DDR3)

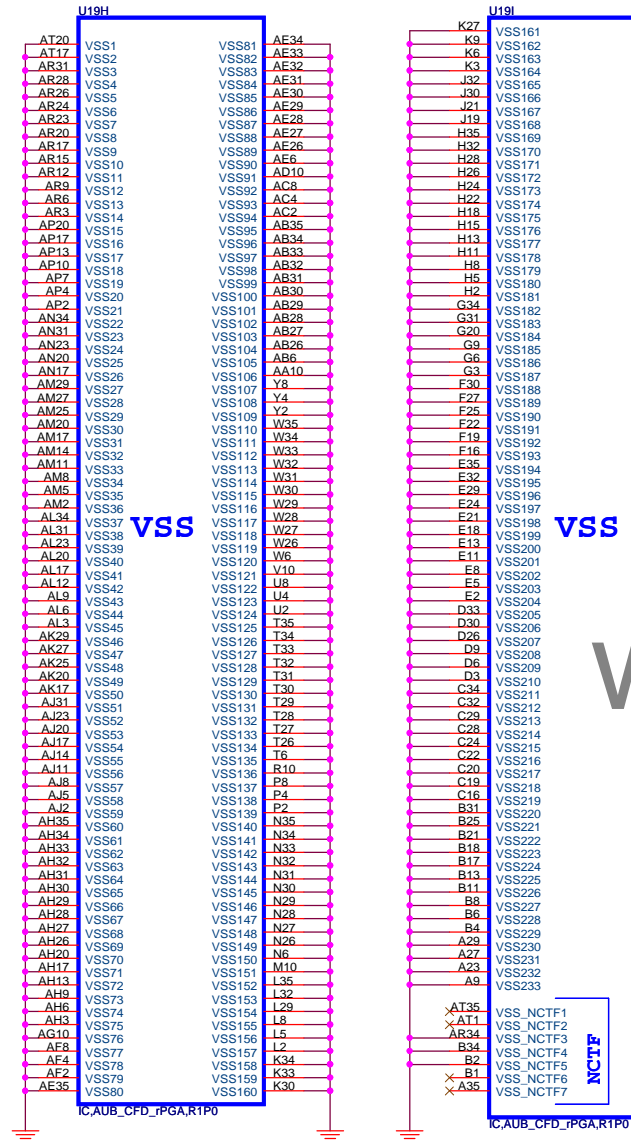


Quanta Computer Inc.

PROJECT : SW6

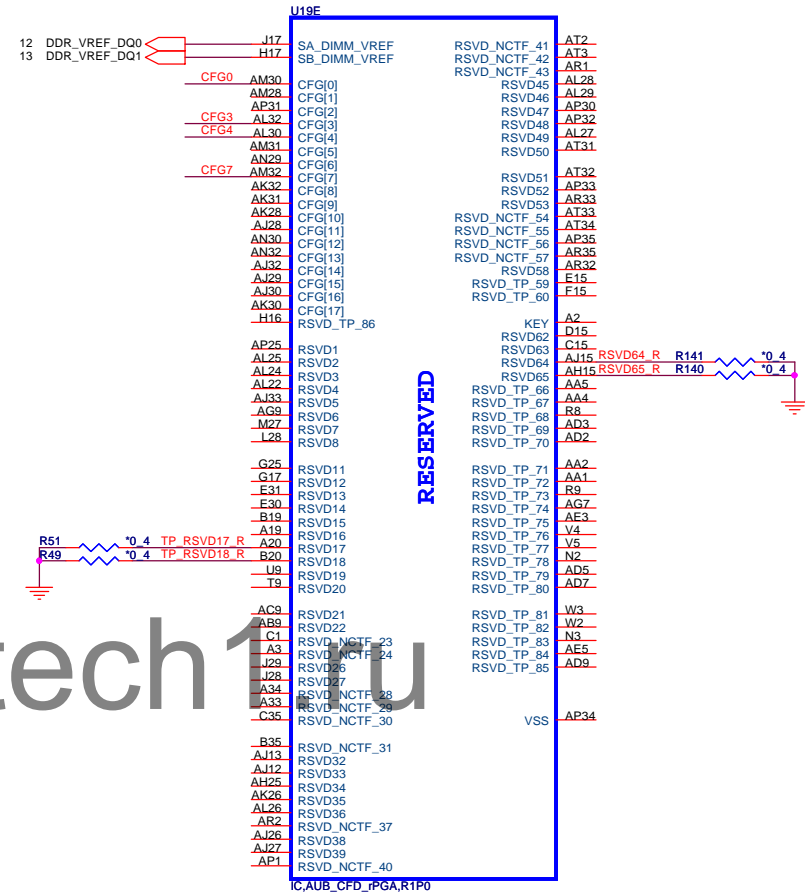


AUBURNDALE/CLARKSFIELD PROCESSOR (GND)



The Clarkfield processor's PCI Express interface may not meet PCI Express 2.0 jitter specifications. Intel recommends placing a 3.01K \pm 5% pull down resistor to VSS on CFG[7] pin for both rPGA and BGA components. This pull down resistor should be removed when this issue is fixed.

AUBURNDALE/CLARKSFIELD PROCESSOR(RESERVED, CFG)



	1	0
CFG4 (Display Port Presence)	Disabled; No Physical Display Port attached to Embedded Display Port	Enabled; An external Display port device is connected to the Embedded Display port
CFG0 (PCI-Epress Configuration Select)	Single PEG	Bifurcation enabled
CFG3 (PCI-Epress Static Lane Reversal)	Normal Operation	Lane Numbers Reversed 15 -> 0 , 14 -> 1

For Discrete only



```
CFG[ 1:0 ] - PCI_Epress Configuration Select
```

```
* 11= 1 x 16 PEG
```

* 10= 2 x 8 PEG



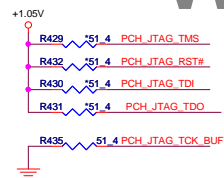
Quanta Computer Inc.

PROJECT : SW6

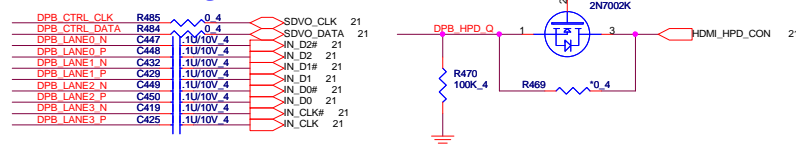
Size	Document Number	Rev
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PROCESSOR 4/4 (GND) A1

Date: Tuesday, May 25, 2010 Sheet 6 of 42

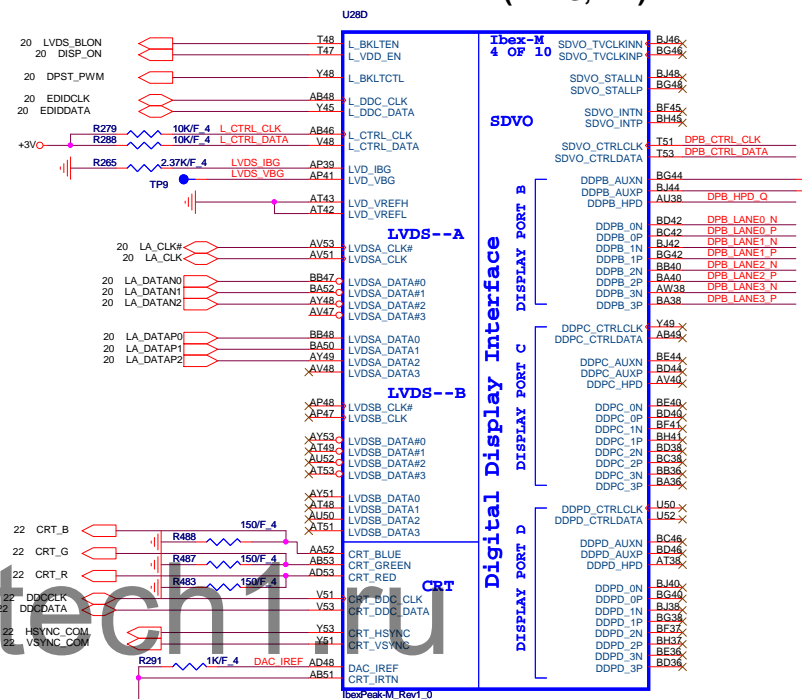


UMA HDMI signals

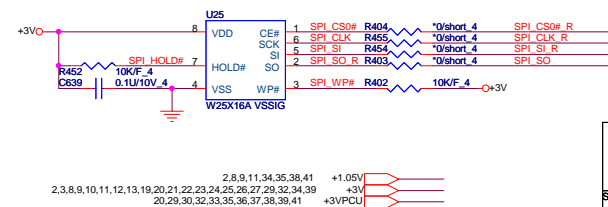


UMA CRT,LVDS&HDMI signals

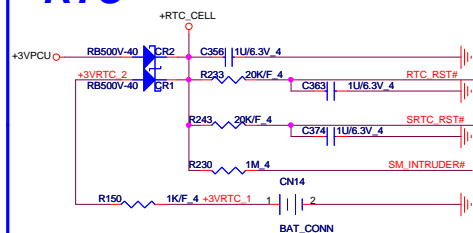
IBEX PEAK-M (LVDS,DDI)



4M byte SPI ROM

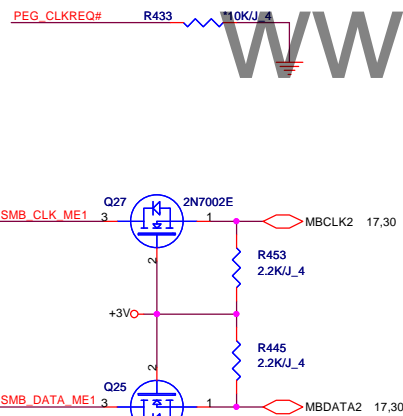
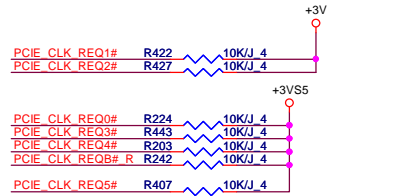
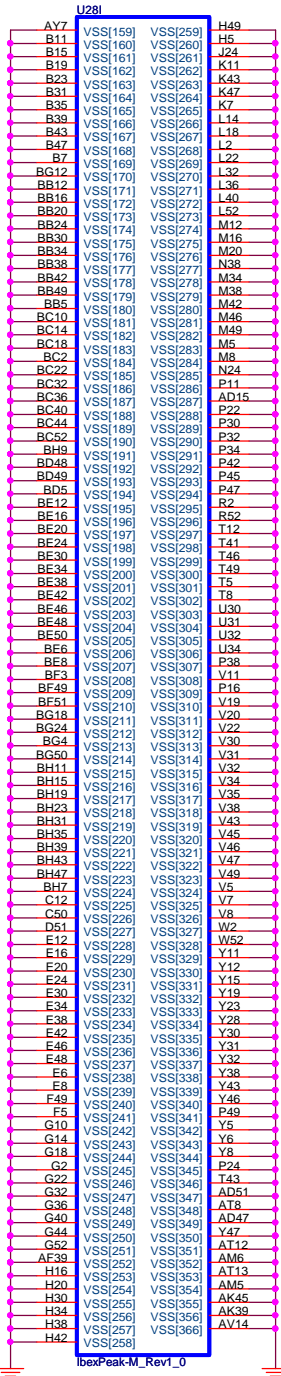


RTC



Vendor	PN
Socket	DG008000031
WINBOND	AKE39ZP0N00
MAX	AKE39FP0N00

IBEX PEAK-M (GND)



I2C

I2C-M 5 OF 10

NVRAM

PCI

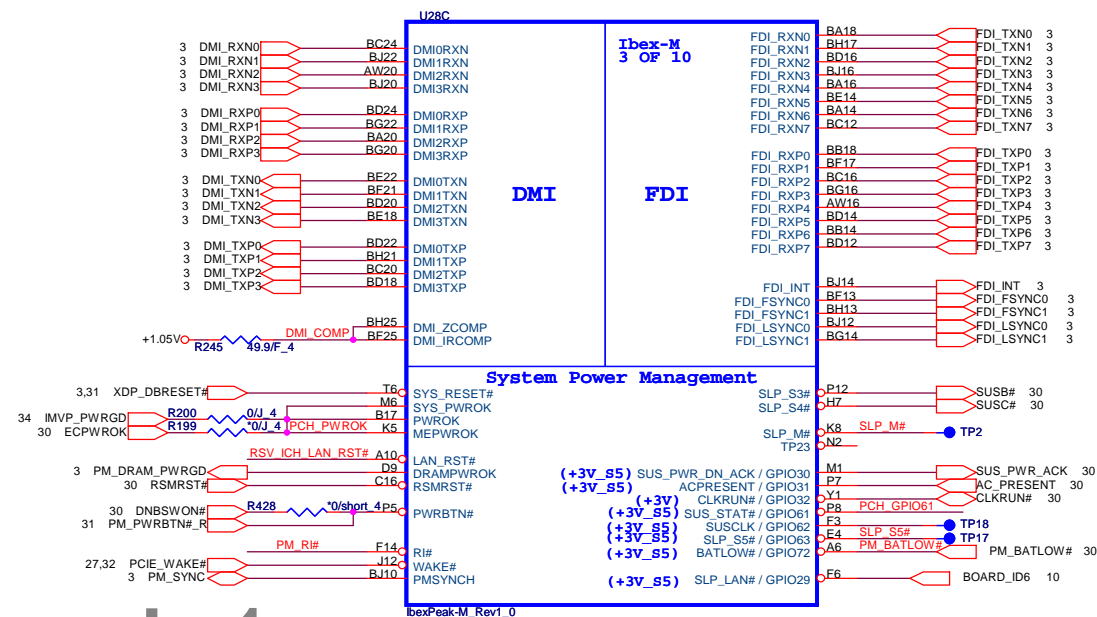
USB

WWW

PexPeak-M Rev1.0

Components and Connections:

- I2C-M 5 OF 10:** H40, N34, C44, A38, A43, A44, A40, A46, A38, A48, M45, F53, M40, M43, J36, K48, F40, C42, K46, M51, J52, K51, L34, F42, J40, A27, G46, F44, A29, A30, H36, A31.
- NVRAM:** NV_CE#0, NV_CE#1, NV_CE#2, NV_CE#3, NV_DQS0, NV_DQS1, NV_DQ0/NV_IO0, NV_DQ1/NV_IO1, NV_DQ2/NV_IO2, NV_DQ3/NV_IO3, NV_DQ4/NV_IO4, NV_DQ5/NV_IO5, NV_DQ6/NV_IO6, NV_DQ7/NV_IO7, NV_DQ8/NV_IO8, NV_DQ9/NV_IO9, NV_DQ10/NV_IO10, NV_DQ11/NV_IO11, NV_DQ12/NV_IO12, NV_DQ13/NV_IO13, NV_DQ14/NV_IO14, NV_DQ15/NV_IO15, NV_ALE, NV_CLE, NV_RCOMP, NV_RB#, NV_WR#0_RE#, NV_WR#1_RE#, NV_WE#_CK0, NV_WE#_CK1.
- PCI:** C/BE0#, C/BE1#, C/BE2#, C/BE3#, PIROA#, PIROB#, PIROC#, PIROD#, REQ0#, REQ1#/GPIO50 (+5V), REQ2#/GPIO52 (+5V), REQ3#/GPIO54 (+5V), GNT0#, GNT1#/GPIO51 (+3V), GNT2#/GPIO53 (+3V), GNT3#/GPIO55 (+3V), PIROE#/GPIO2 (+5V), PIROF#/GPIO3 (+5V), PIROG#/GPIO4 (+5V), PIROH#/GPIO5 (+5V), PCIRST#, SERR#, F40, PERR#, IRDY#, A42, PAR, DEVSEL#, F46, FRAME#, C46, PLOCK#, D49, STOP#, D41, TRDY#, C48, ME#, M7, RST-R#, D5, PLTRST#, CLKOUT_PCIO, CLKOUT_PC1, CLKOUT_PC2, CLKOUT_PC3, CLKOUT_PC4.
- USB:** USBP0#, USBP1#, USBP2#, USBP3#, USBP4#, USBP5#, USBP6#, USBP7#, USBP8#, USBP9#, USBP10#, USBP11#, USBP12#, USBP13#, USBP13P, H18, A18, A19, A20, A21, A22, A23, A24, A25, A26, A27, A28, A29, A30, A31, A32, A33, A34, A35, A36, A37, A38, A39, A40, A41, A42, A43, A44, A45, A46, A47, A48, A49, A50, A51, A52, A53, A54, A55, A56, A57, A58, A59, A60, A61, A62, A63, A64, A65, A66, A67, A68, A69, A70, A71, A72, A73, A74, A75, A76, A77, A78, A79, A80, A81, A82, A83, A84, A85, A86, A87, A88, A89, A90, A91, A92, A93, A94, A95, A96, A97, A98, A99, A100, A101, A102, A103, A104, A105, A106, A107, A108, A109, A110, A111, A112, A113, A114, A115, A116, A117, A118, A119, A120, A121, A122, A123, A124, A125, A126, A127, A128, A129, A130, A131, A132, A133, A134, A135, A136, A137, A138, A139, A140, A141, A142, A143, A144, A145, A146, A147, A148, A149, A150, A151, A152, A153, A154, A155, A156, A157, A158, A159, A160, A161, A162, A163, A164, A165, A166, A167, A168, A169, A170, A171, A172, A173, A174, A175, A176, A177, A178, A179, A180, A181, A182, A183, A184, A185, A186, A187, A188, A189, A190, A191, A192, A193, A194, A195, A196, A197, A198, A199, A200, A201, A202, A203, A204, A205, A206, A207, A208, A209, A210, A211, A212, A213, A214, A215, A216, A217, A218, A219, A220, A221, A222, A223, A224, A225, A226, A227, A228, A229, A230, A231, A232, A233, A234, A235, A236, A237, A238, A239, A240, A241, A242, A243, A244, A245, A246, A247, A248, A249, A250, A251, A252, A253, A254, A255, A256, A257, A258, A259, A260, A261, A262, A263, A264, A265, A266, A267, A268, A269, A270, A271, A272, A273, A274, A275, A276, A277, A278, A279, A280, A281, A282, A283, A284, A285, A286, A287, A288, A289, A290, A291, A292, A293, A294, A295, A296, A297, A298, A299, A300, A301, A302, A303, A304, A305, A306, A307, A308, A309, A310, A311, A312, A313, A314, A315, A316, A317, A318, A319, A320, A321, A322, A323, A324, A325, A326, A327, A328, A329, A330, A331, A332, A333, A334, A335, A336, A337, A338, A339, A340, A341, A342, A343, A344, A345, A346, A347, A348, A349, A350, A351, A352, A353, A354, A355, A356, A357, A358, A359, A360, A361, A362, A363, A364, A365, A366, A367, A368, A369, A370, A371, A372, A373, A374, A375, A376, A377, A378, A379, A380, A381, A382, A383, A384, A385, A386, A387, A388, A389, A390, A391, A392, A393, A394, A395, A396, A397, A398, A399, A400, A401, A402, A403, A404, A405, A406, A407, A408, A409, A410, A411, A412, A413, A414, A415, A416, A417, A418, A419, A420, A421, A422, A423, A424, A425, A426, A427, A428, A429, A430, A431, A432, A433, A434, A435, A436, A437, A438, A439, A440, A441, A442, A443, A444, A445, A446, A447, A448, A449, A450, A451, A452, A453, A454, A455, A456, A457, A458, A459, A460, A461, A462, A463, A464, A465, A466, A467, A468, A469, A470, A471, A472, A473, A474, A475, A476, A477, A478, A479, A480, A481, A482, A483, A484, A485, A486, A487, A488, A489, A490, A491, A492, A493, A494, A495, A496, A497, A498, A499, A500, A501, A502, A503, A504, A505, A506, A507, A508, A509, A510, A511, A512, A513, A514, A515, A516, A517, A518, A519, A520, A521, A522, A523, A524, A525, A526, A527, A528, A529, A530, A531, A532, A533, A534, A535, A536, A537, A538, A539, A540, A541, A542, A543, A544, A545, A546, A547, A548, A549, A550, A551, A552, A553, A554, A555, A556, A557, A558, A559, A560, A561, A562, A563, A564, A565, A566, A567, A568, A569, A570, A571, A572, A573, A574, A575, A576, A577, A578, A579, A580, A581, A582, A583, A584, A585, A586, A587, A588, A589, A590, A591, A592, A593, A594, A595, A596, A597, A598, A599, A600, A601, A602, A603, A604, A605, A606, A607, A608, A609, A610, A611, A612, A613, A614, A615, A616, A617, A618, A619, A620, A621, A622, A623, A624, A625, A626, A627, A628, A629, A630, A631, A632, A633, A634, A635, A636, A637, A638, A639, A640, A641, A642, A643, A644, A645, A646, A647, A648, A649, A650, A651, A652, A653, A654, A655, A656, A657, A658, A659, A660, A661, A662, A663, A664, A665, A666, A667, A668, A



NV_WE#_CK1 BF5
 USBP0N LH18 USBP0- 28 LEFT SIDE USB #0
 USBP0P L18 USBP0+ 28
 USBP1N L18 USBP1- 28 LEFT SIDE USB #1
 USBP1P C18 USBP1+ 28
 USBP2N N20

LEFT SIDE USB #0
LEFT SIDE USB #1

Webcam

EXT USB #3

WLAN

CR


USB

100

1

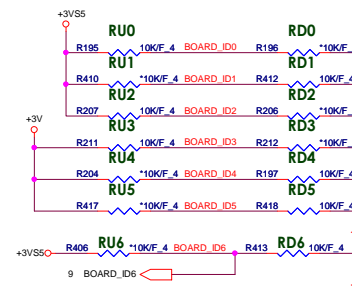
(+3V_S5)

$$\begin{Bmatrix} +3V \\ +3V \\ +3V \end{Bmatrix}_{S5}$$

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Size	Document Number PCH3/5 (PCI,ONFI,USB,DMI)	
Date:	Tuesday, May 25, 2010	Sheet 9 of 42

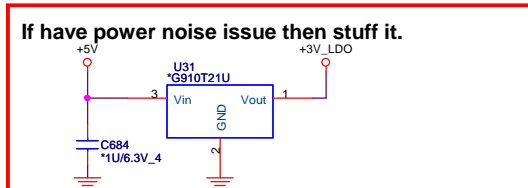
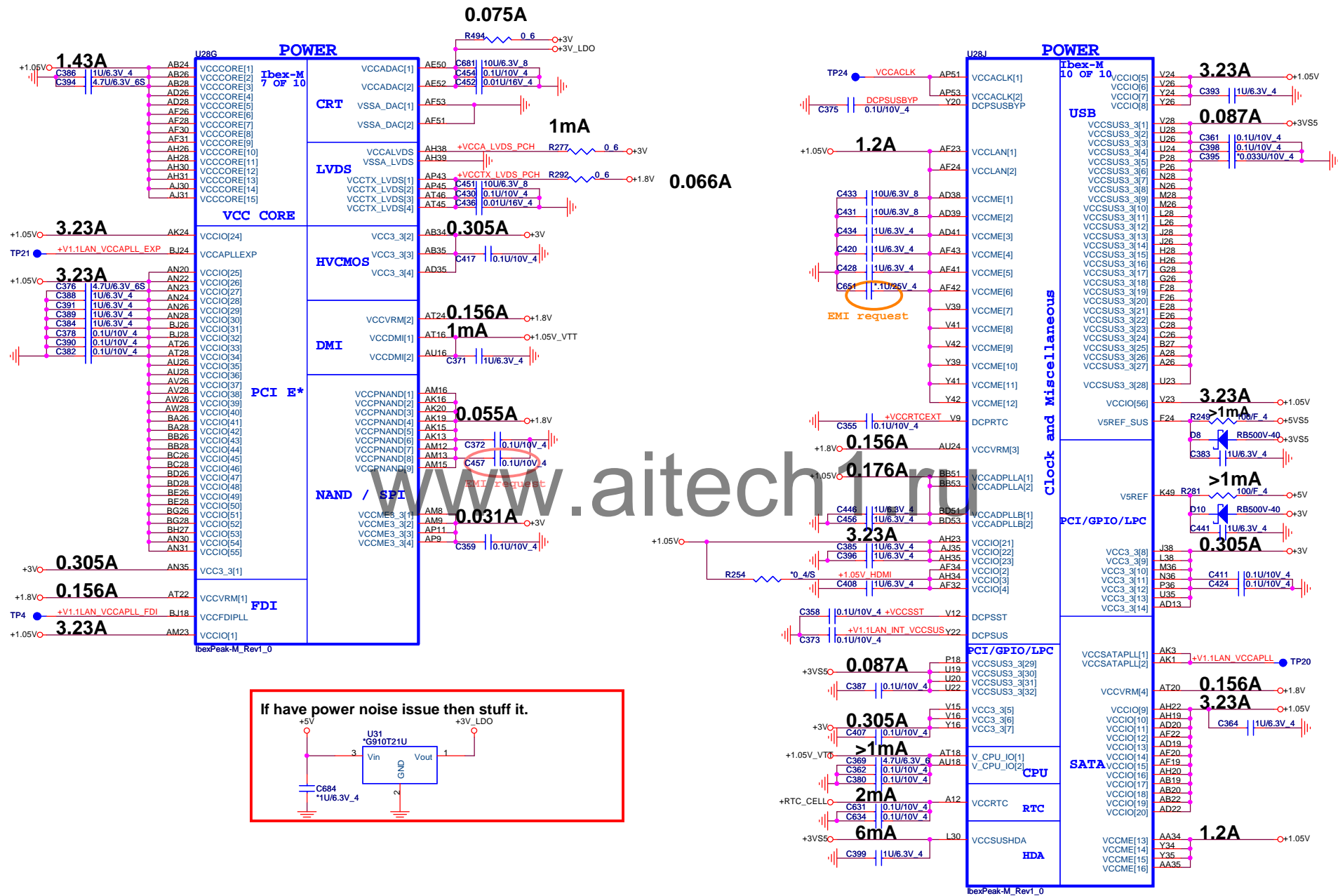
IBEX PEAK-M (GND)

```
Board ID5: Switchable
Board ID4: Dobly
Board ID3: MDC
Board ID2: 14'' / 15.6"
Board ID1: UMA / DIS
Board ID0: LG / CB
```

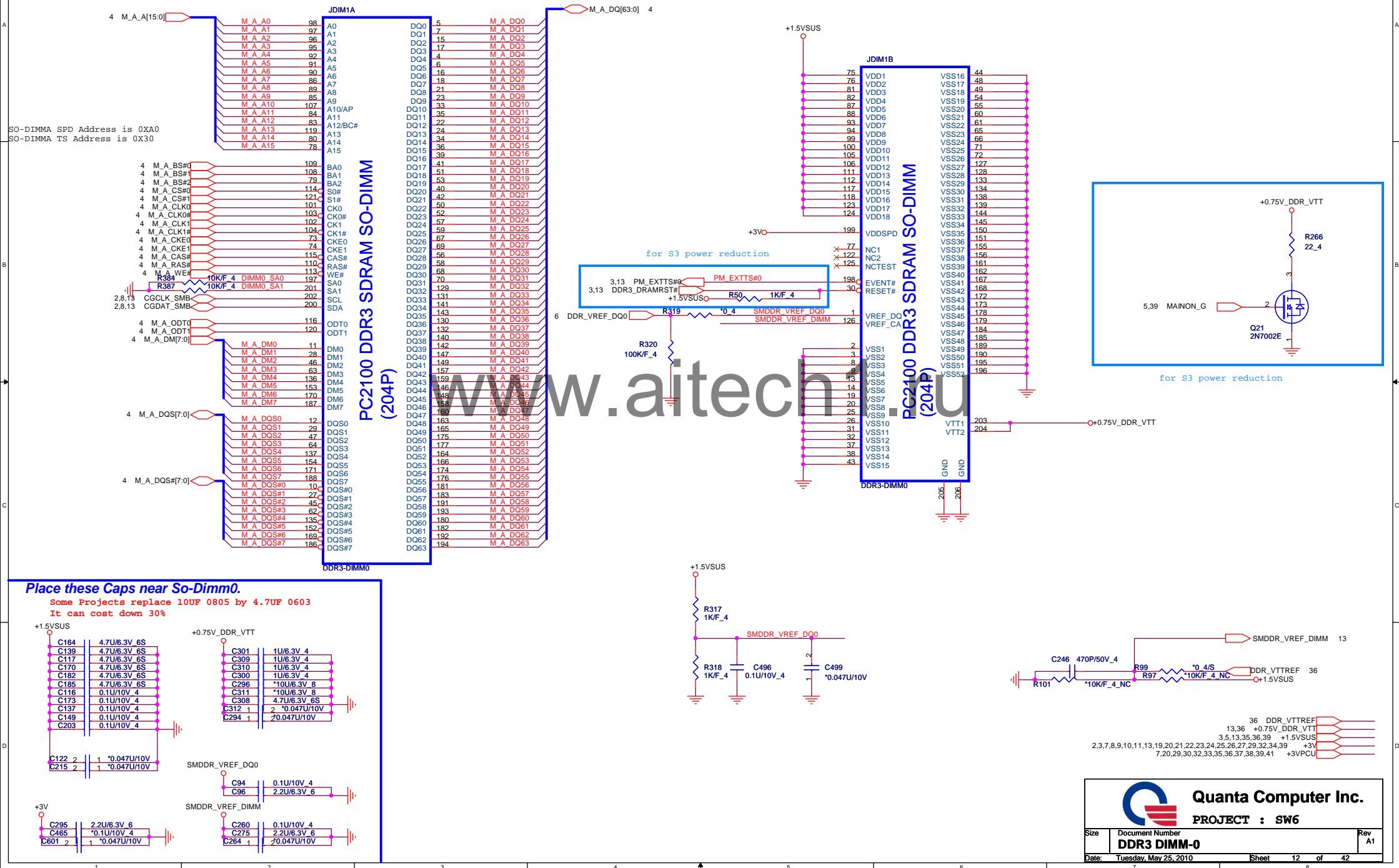
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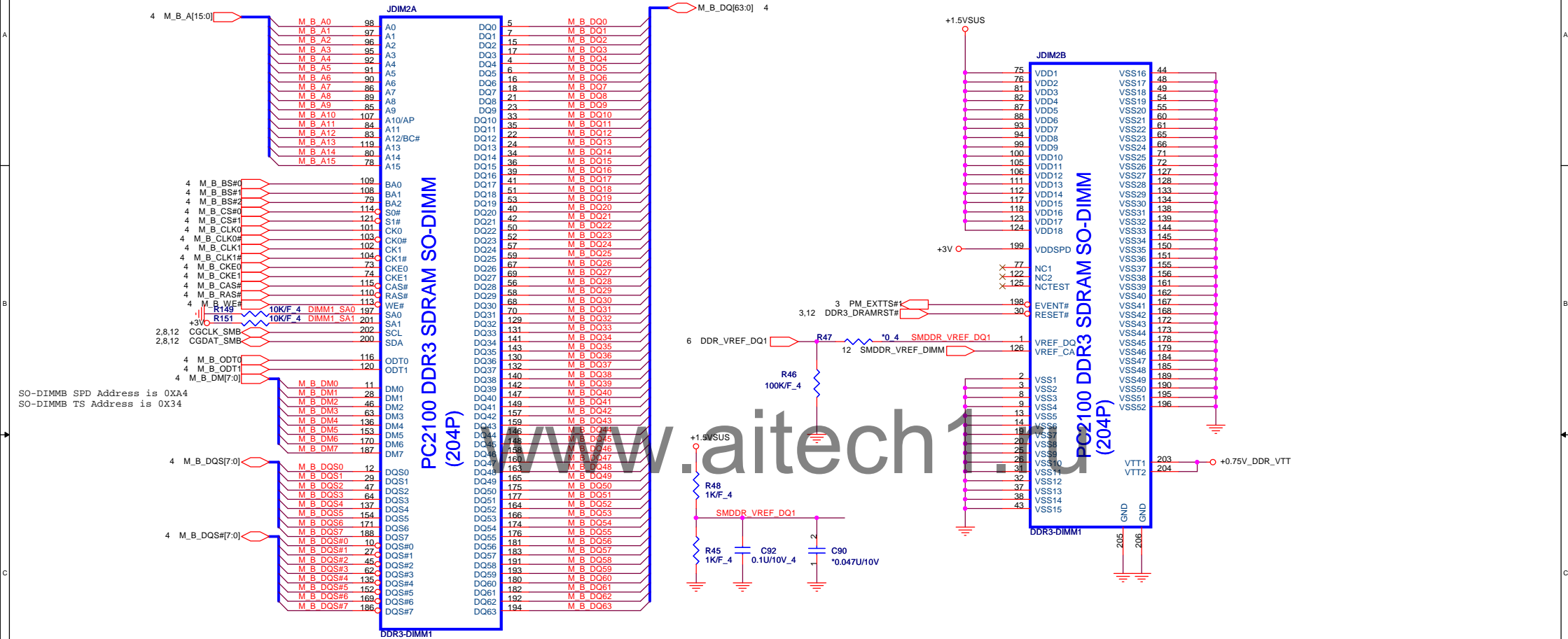
PROJECT : SW6

Size	Document Number PCH 4/5 (GPIO & Strap)			R
Date:	Tuesday, May 25, 2010	Sheet	10 of 42	



7 +RTC_CELL
2,7,8,9,34,35,38,41 +1.05V
3,5,10,30,31,34,40,41 +1.05V_VTT
5,10,35,38,39 +1.8V
2,3,7,8,9,10,12,13,19,20,21,22,23,24,25,26,27,29,32,34,39 +3V
3,8,9,10,19,39 +3VS5
21,22,23,25,26,29,32,39 +5V
33,39 +5VS5



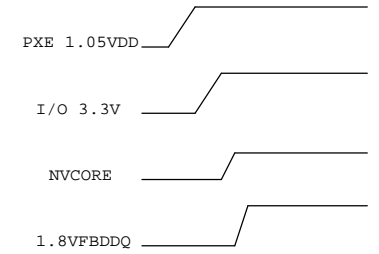


Place these Caps near So-Dimm1.

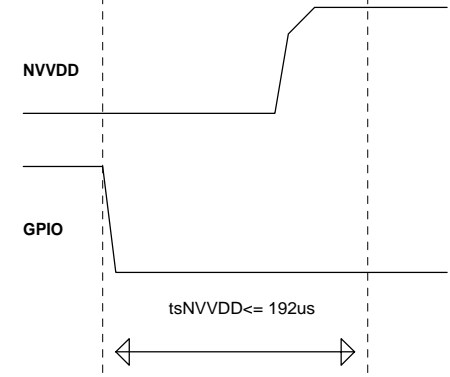
Some Projects replace 10UF 0805 by 4.7UF 0603
It can cost down 30%



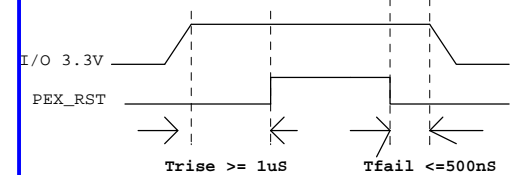
power up sequence



NB9M: VGACORE +0.90V (Normal) , +1.09V
NVVDD Maximum Settling Time



PEX_RST timing



17,19,30,35 +3V_GFX
15,16,35 +1.05V_GFX
35 +VGACORE

U21A

PBGAS33-NVIDIA-GEFORCEG250
COMMON

1/13 PCL EXPRESS

(NC) PEX_CLKREQ

PEX_RST*

PEX_REFCLK

PEX_REFCLK*

PEX_TX0

PEX_TX0*

PEX_TX1

PEX_TX1*

PEX_TX2

PEX_TX2*

PEX_TX3

PEX_TX3*

PEX_TX4

PEX_TX4*

PEX_TX5

PEX_TX5*

PEX_TX6

PEX_TX6*

PEX_TX7

PEX_TX7*

PEX_TX8

PEX_TX8*

PEX_TX9

PEX_TX9*

PEX_TX10

PEX_TX10*

PEX_TX11

PEX_TX11*

PEX_TX12

PEX_TX12*

PEX_TX13

PEX_TX13*

PEX_TX14

PEX_TX14*

PEX_TX15

PEX_TX15*

PEX_RX0

PEX_RX0*

PEX_RX1

PEX_RX1*

PEX_RX2

PEX_RX2*

PEX_RX3

PEX_RX3*

PEX_RX4

PEX_RX4*

PEX_RX5

PEX_RX5*

PEX_RX6

PEX_RX6*

PEX_RX7

PEX_RX7*

PEX_RX8

PEX_RX8*

PEX_RX9

PEX_RX9*

PEX_RX10

PEX_RX10*

PEX_RX11

PEX_RX11*

PEX_RX12

PEX_RX12*

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PEX_RX15

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PEX_RX31

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PEX_RX48

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PEX_RX50*

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PEX_RX51*

PEX_RX52

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PEX_RX53*

PEX_RX54

PEX_RX54*

PEX_RX55

PEX_RX55*

PEX_RX56

PEX_RX56*

PEX_RX57

PEX_RX57*

PEX_RX58

PEX_RX58*

PEX_RX59

PEX_RX59*

PEX_RX60

PEX_RX60*

PEX_RX61

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PEX_RX62

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PEX_RX63

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PEX_RX64

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PEX_RX69*

PEX_RX70

PEX_RX70*

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PEX_RX74

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PEX_RX75*

PEX_RX76

PEX_RX76*

PEX_RX77

PEX_RX77*

PEX_RX78

PEX_RX78*

PEX_RX79

PEX_RX79*

PEX_RX80

PEX_RX80*

PEX_RX81

PEX_RX81*

PEX_RX82

PEX_RX82*

PEX_RX83

PEX_RX83*

PEX_RX84

PEX_RX84*

PEX_RX85

PEX_RX85*

PEX_RX86

PEX_RX86*

PEX_RX87

PEX_RX87*

PEX_RX88

PEX_RX88*

PEX_RX89

PEX_RX89*

PEX_RX90

PEX_RX90*

PEX_RX91

PEX_RX91*

PEX_RX92

PEX_RX92*

PEX_RX93

PEX_RX93*

PEX_RX94

PEX_RX94*

PEX_RX95

PEX_RX95*

PEX_RX96

PEX_RX96*

PEX_RX97

PEX_RX97*

PEX_RX98

PEX_RX98*

PEX_RX99

PEX_RX99*

PEX_RX100

PEX_RX100*

PEX_RX101

PEX_RX101*

PEX_RX102

PEX_RX102*

PEX_RX103

PEX_RX103*

PEX_RX104

PEX_RX104*

PEX_RX105

PEX_RX105*

PEX_RX106

PEX_RX106*

PEX_RX107

PEX_RX107*

PEX_RX108

PEX_RX108*

PEX_RX109

PEX_RX109*

PEX_RX110

PEX_RX110*

PEX_RX111

PEX_RX111*

PEX_RX112

PEX_RX112*

PEX_RX113

PEX_RX113*

PEX_RX114

PEX_RX114*

PEX_RX115

PEX_RX115*

PEX_RX116

PEX_RX116*

PEX_RX117

PEX_RX117*

PEX_RX118

PEX_RX118*

PEX_RX119

PEX_RX119*

PEX_RX120

PEX_RX120*

PEX_RX121

PEX_RX121*

PEX_RX122

PEX_RX122*

PEX_RX123

PEX_RX123*

PEX_RX124

PEX_RX124*

PEX_RX125

PEX_RX125*

PEX_RX126

PEX_RX126*

PEX_RX127

PEX_RX127*

PEX_RX128

PEX_RX128*

PEX_RX129

PEX_RX129*

PEX_RX130

PEX_RX130*

PEX_RX131

PEX_RX131*

PEX_RX132

PEX_RX132*

PEX_RX133

PEX_RX133*

PEX_RX134

PEX_RX134*

PEX_RX135

PEX_RX135*

PEX_RX136

PEX_RX136*

PEX_RX137

PEX_RX137*

PEX_RX138

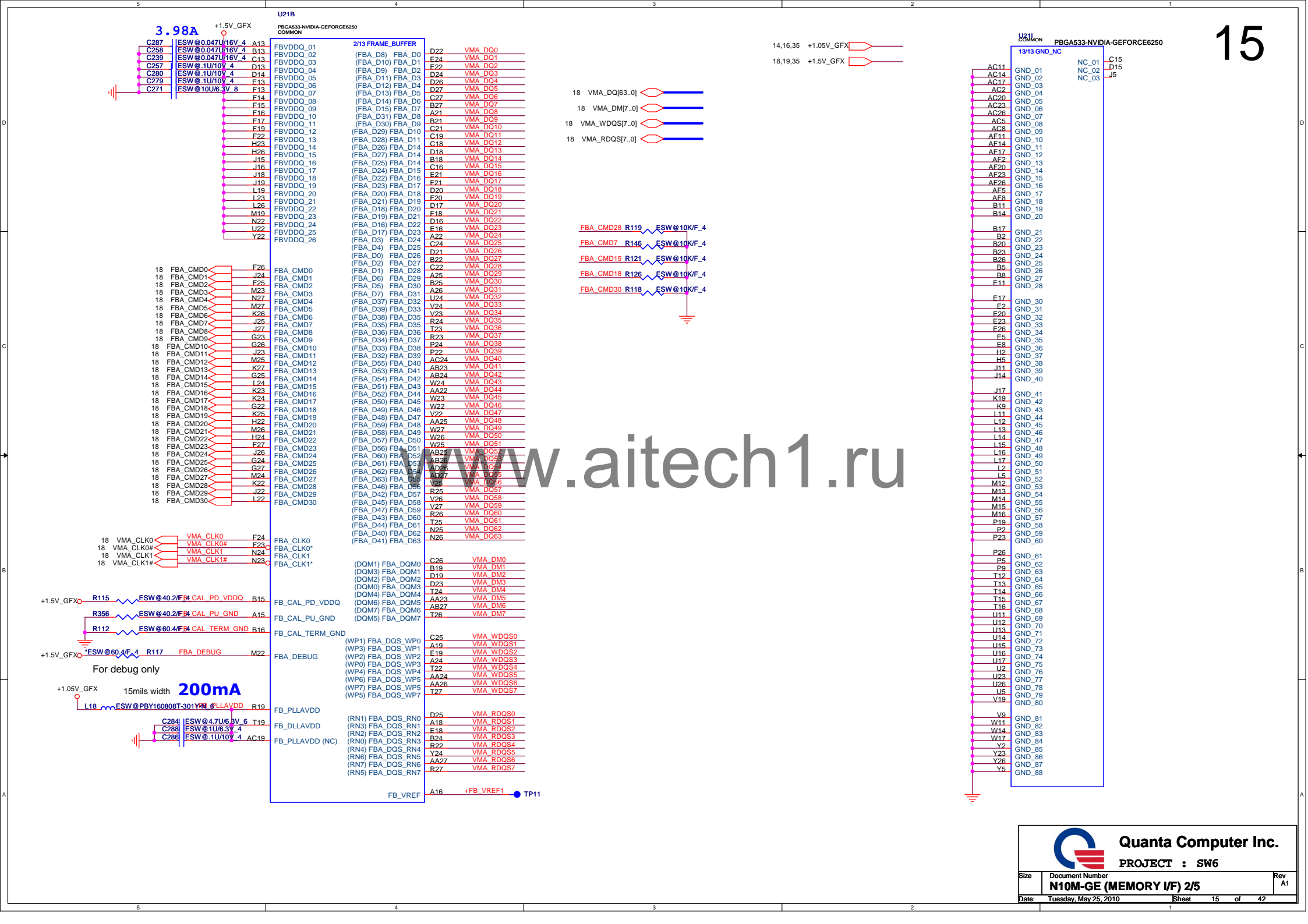
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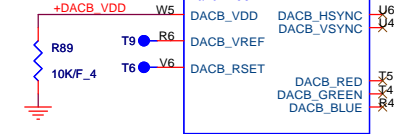
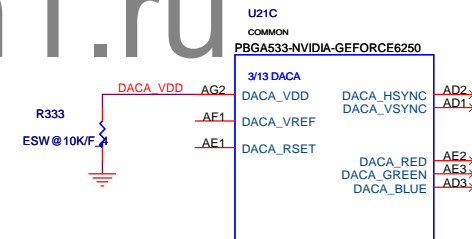
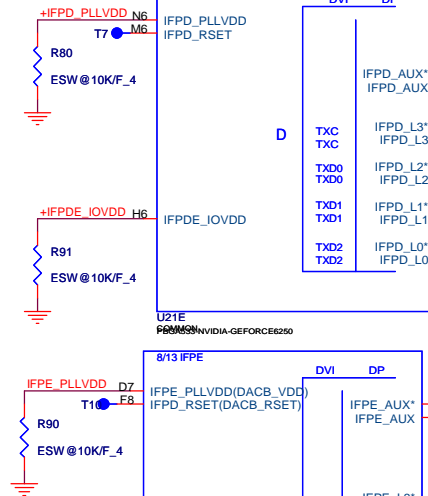
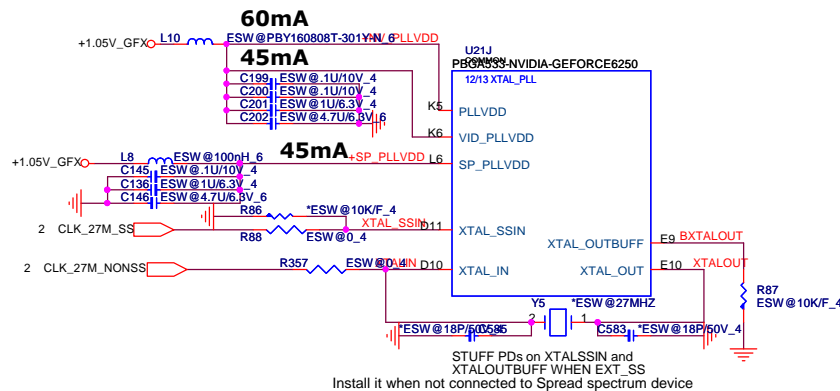
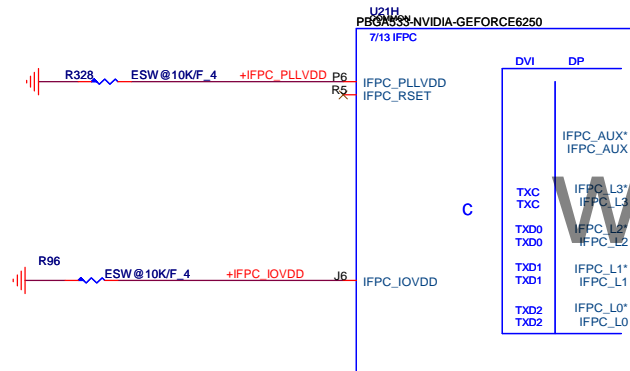
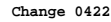
PEX_RX139

PEX_RX139*

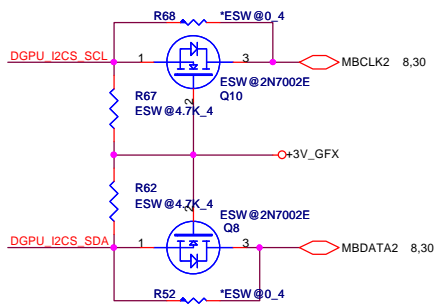
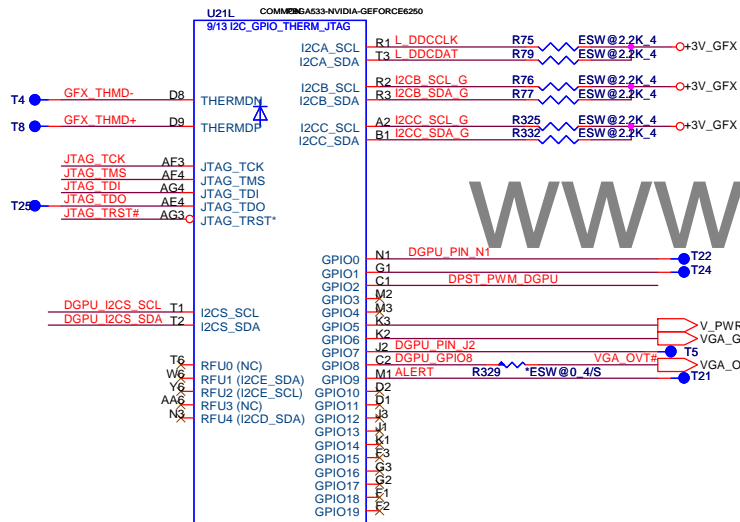
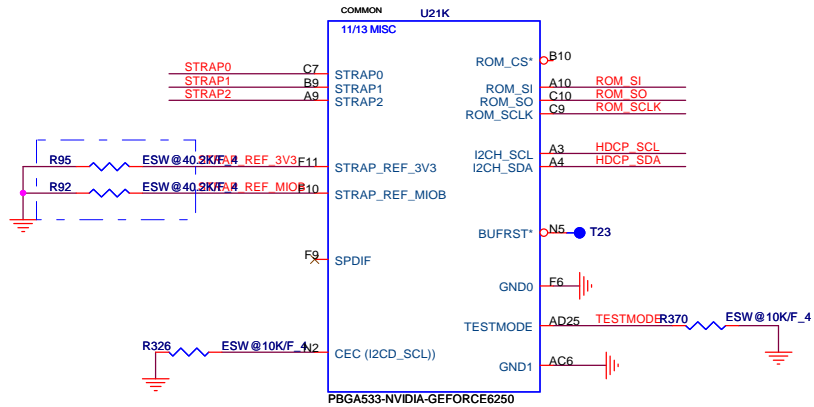
PEX_RX140

PEX_RX140*





SPREAD SPECTRUM == >Del 6/16



Mount Q15, Q30, R801, R802
For Switchable only

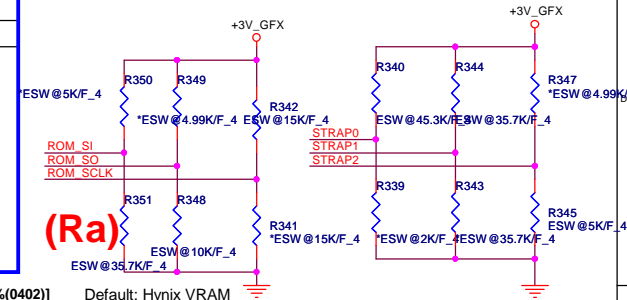
14,19,30,35 +3V_GFX

CHIP	PCI_DEVID:	STRAP2
NB11M-GE2	0x0A70	0000 PD 5K

Logical Strap Bit Mapping		
	PU-VDD	PD
5K	1000	0000
10K	1001	0001
15K	1010	0010
20K	1011	0011
25K	1100	0100
30K	1101	0101
35K	1110	0110
45K	1111	0111

4.99K/F 4: CS24992FB26 [RES CHIP 4.99K 1/16W +1% (0402)]
10K/F 4: CS31002FB26 [RES CHIP 10K 1/16W +1% (0402)]
15K/F 4: CS31502FB24 [RES CHIP 15K 1/16W +1% (0402)]
30.1K/F 4: CS33012FB18 [RES CHIP 30.1K 1/16W +1% (0402)]
35.7K/F 4: CS33572FB13 [RES CHIP 35.7K 1/16W +1% (0402)]
45.3K/F 4: CS34532FB18 [RES CHIP 45.3K 1/16W +1% (0402)]

PCI_DEVID[4]/SUBVENDOR



Default: Hynix VRAM

NB11M-GE2	Logical Strapping Bit3	Logical Strapping Bit2	Logical Strapping Bit1	Logical Strapping Bit0	
ROM_SO	XCLK_417	FB_0_BAR_SIZE	SMB_ALT_ADDR	VGA_DEVICE	0001
ROM_SCLK	PCI_DEVIDE[4]	SUB_VENDOR	SLOT_CLK_CFG	PEX_PLL_EN_TERM	1010
ROM_SI	RAMCFG[3]	RAMCFG[2]	RAMCFG[1]	RAMCFG[0]	XXXX
STRAP2	PCI_DEVID[3]	PCI_DEVID[2]	PCI_DEVID[1]	PCI_DEVID[0]	0000
STRAP1	3GIO_PADCFG[3]	3GIO_PADCFG[2]	3GIO_PADCFG[1]	3GIO_PADCFG[0]	1110
STRAP0	USER[3]	USER[2]	USER[1]	USER[0]	1111

VRAM Configuration Table

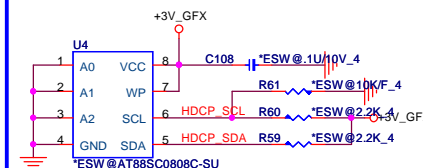
RAMCFG [3:0]	DESCRIPTION	Vendor	Vendor P/N	ROM_SI
0110	DDR3 128Mx16x8, 64bit, 1GB,800MHz	Hynix	H5TQ2G63BFR-12C	PD 35K
0111	DDR3 128Mx16x8, 64bit, 1GB,800MHz	Samsung	K4W2G1646B-HC12	PD 45K

(Ra)

GPIO ASSIGNMENTS

GPIO	I/O	ACTIVE	USAGE
0	N/A	N/A	
1	IN	N/A	Hot plug detect for IFP link C
2	OUT	HIGH	PANEL BACKLIGHT PWM
3	OUT	HIGH	PANEL POWER ENABLE
4	OUT	HIGH	PANEL BACKLIGHT ENABLE
5	OUT	N/A	NVVD VID0
6	OUT	N/A	NVVD VID1
7	OUT	N/A	NVVD VID2 11/13
8	I/O	LOW	OVERT
9	I/O	LOW	ALERT
10	OUT	N/A	FBVREF SELECT
11	OUT	N/A	SLI SYNC0
12	IN	N/A	PWR_LEVEL 11/13
13	OUT	N/A	MEM_VID or power supply control
14	OUT	N/A	PS CONTROL

HDCP ROM



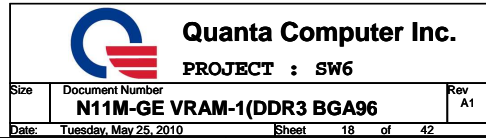
DHCP ROM	
HDCP_SCL	Low: Crypto ROM Hi: I2C ROM



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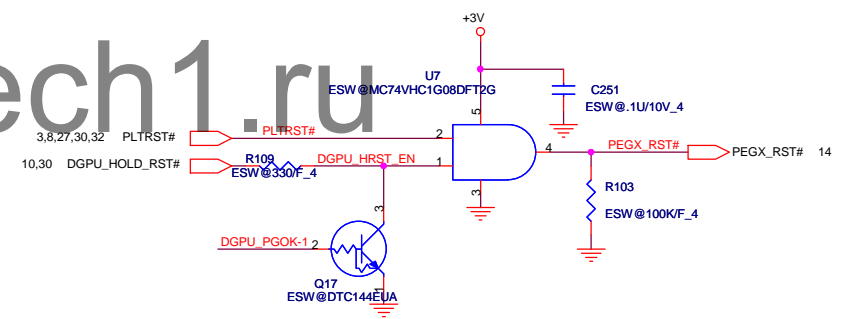
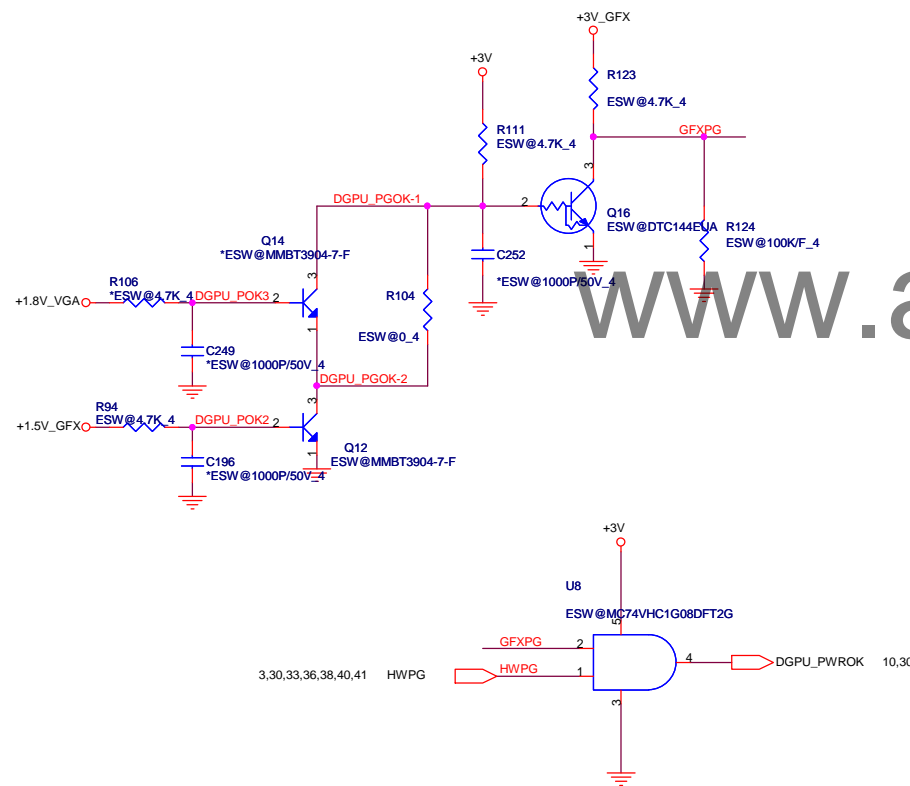
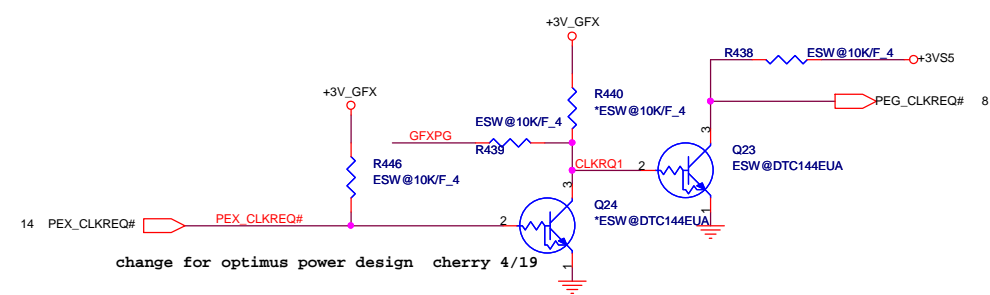
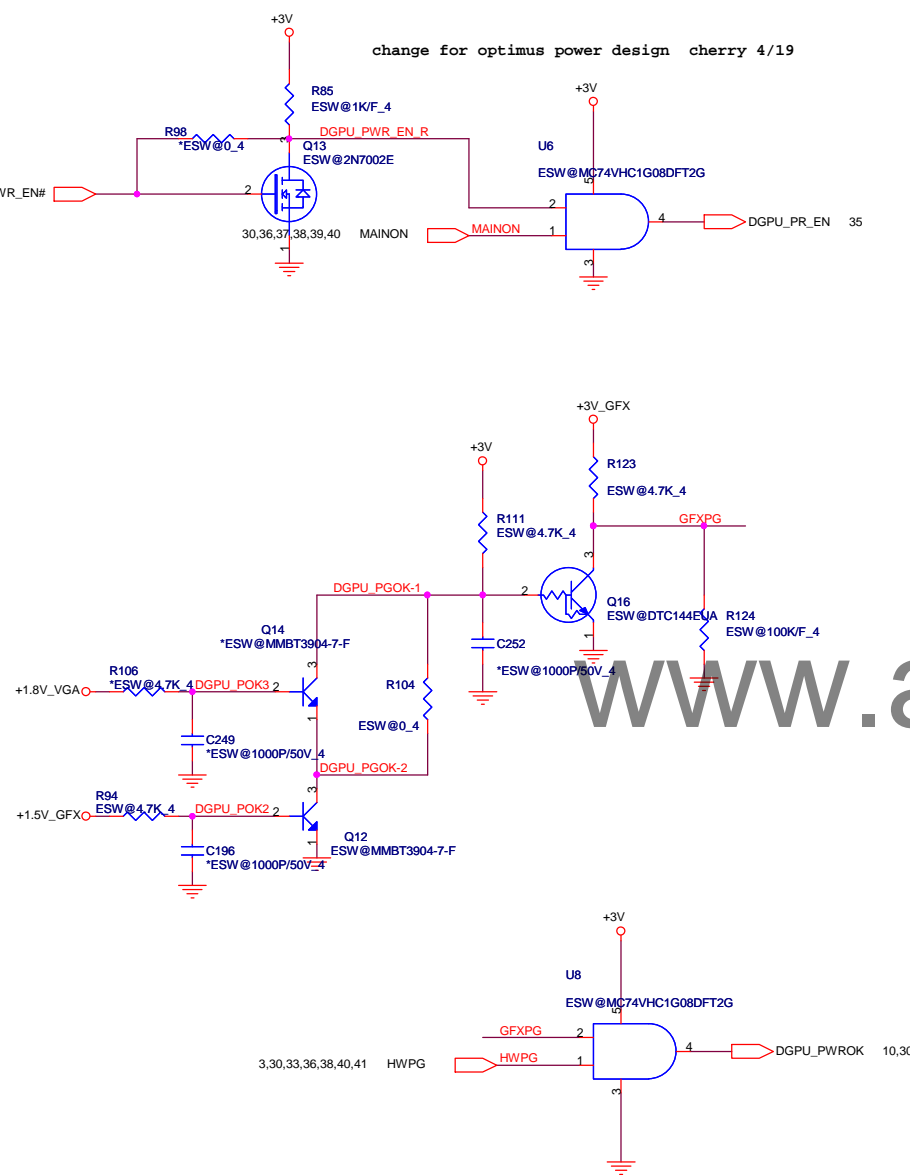
PROJECT : SW6

Size	Document Number	Rev
	N10M-GE (GPIO&STRAPS) 4/5	A1
Date:	Tuesday, May 25, 2010	Sheet 17 of 42



change for optimus power design cherry 4/19

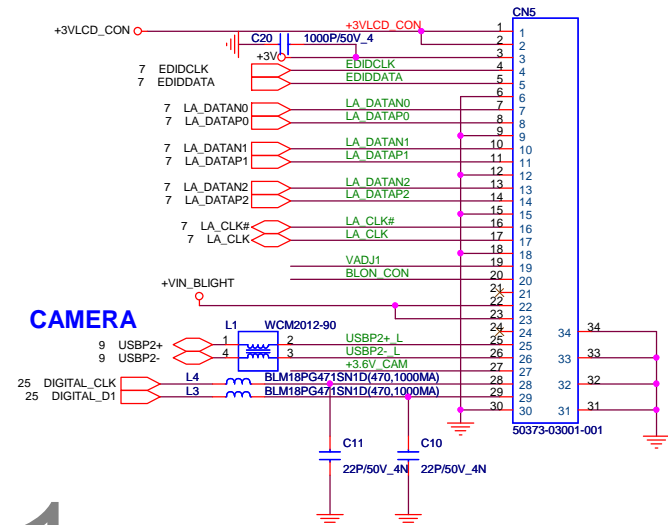
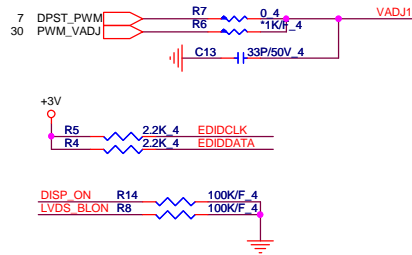
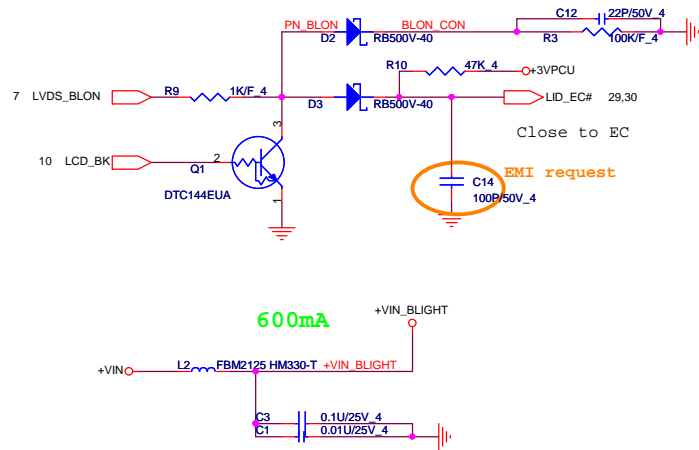
change for optimus power design cherry 4/19



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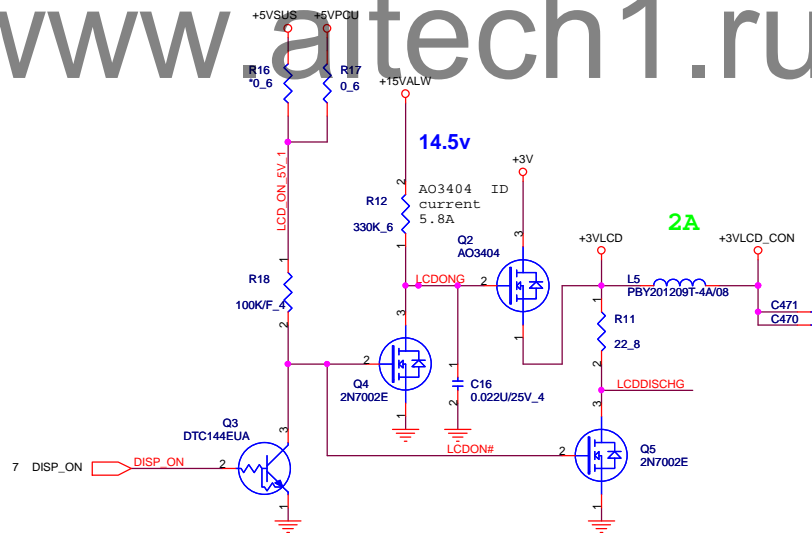
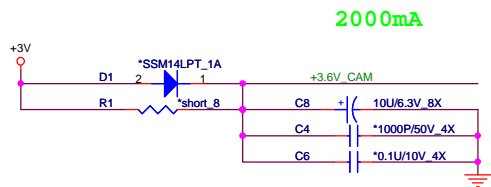
LID Switch

20



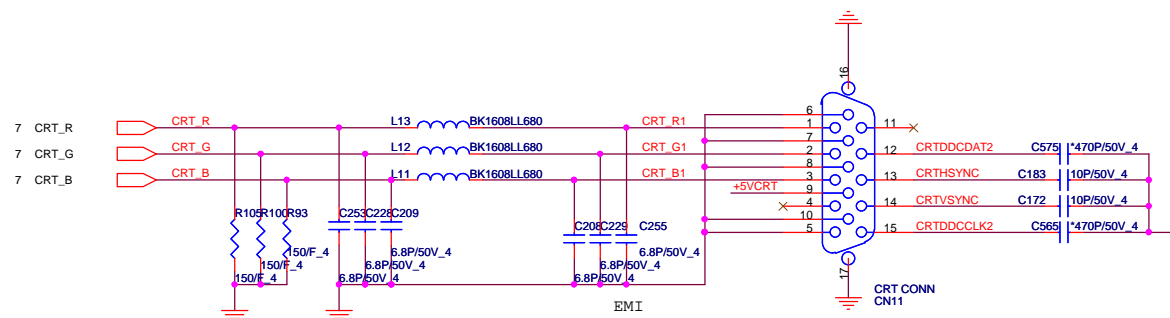
CAMERA

Keep same as TE2's

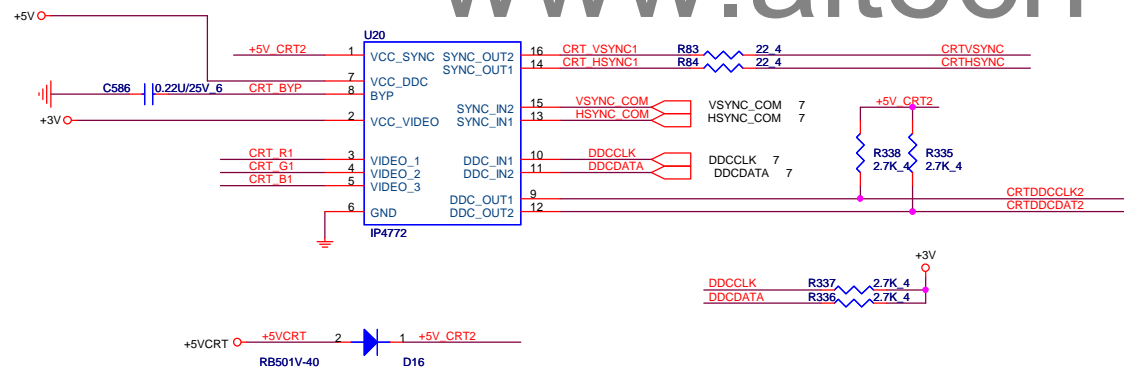


2,3,7,8,9,10,11,12,13,19,21,22,23,24,25,26,27,29,32,34,39
7,29,30,32,33,35,36,37,38,39,41
11,21,22,23,25,26,29,32,39
29,39
33,35,39
33,34,35,36,37,38,39,41

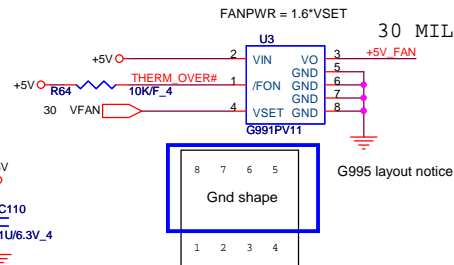
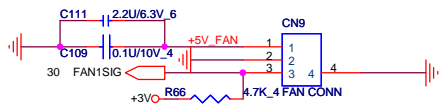
+3V
+3VPCU
+5V
+5VSUS
+15VALW
+VIN



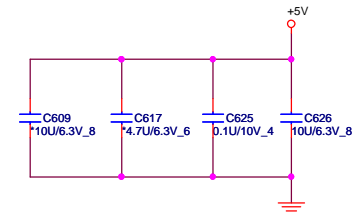
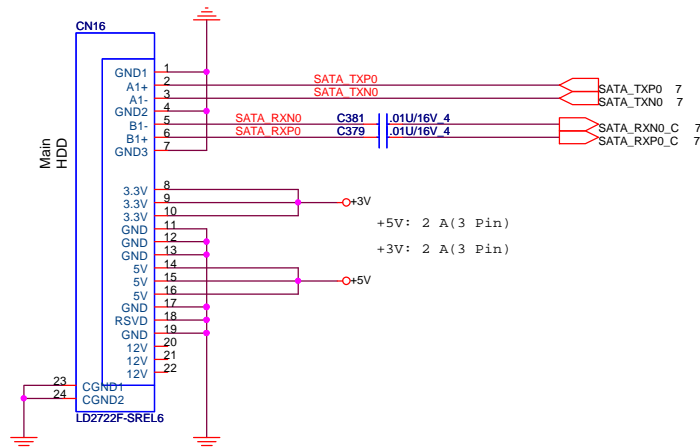
www.aitech1.ru



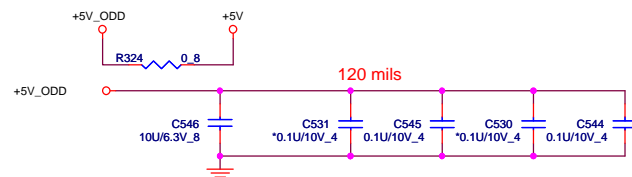
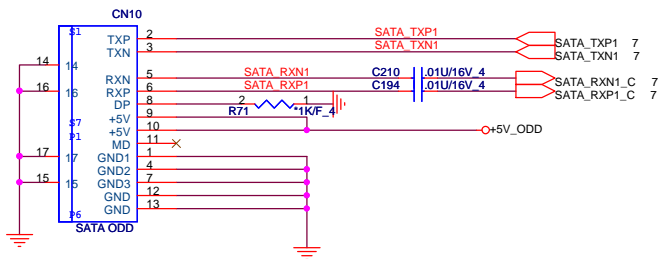
CPU FAN



SATA HDD CONNECTOR

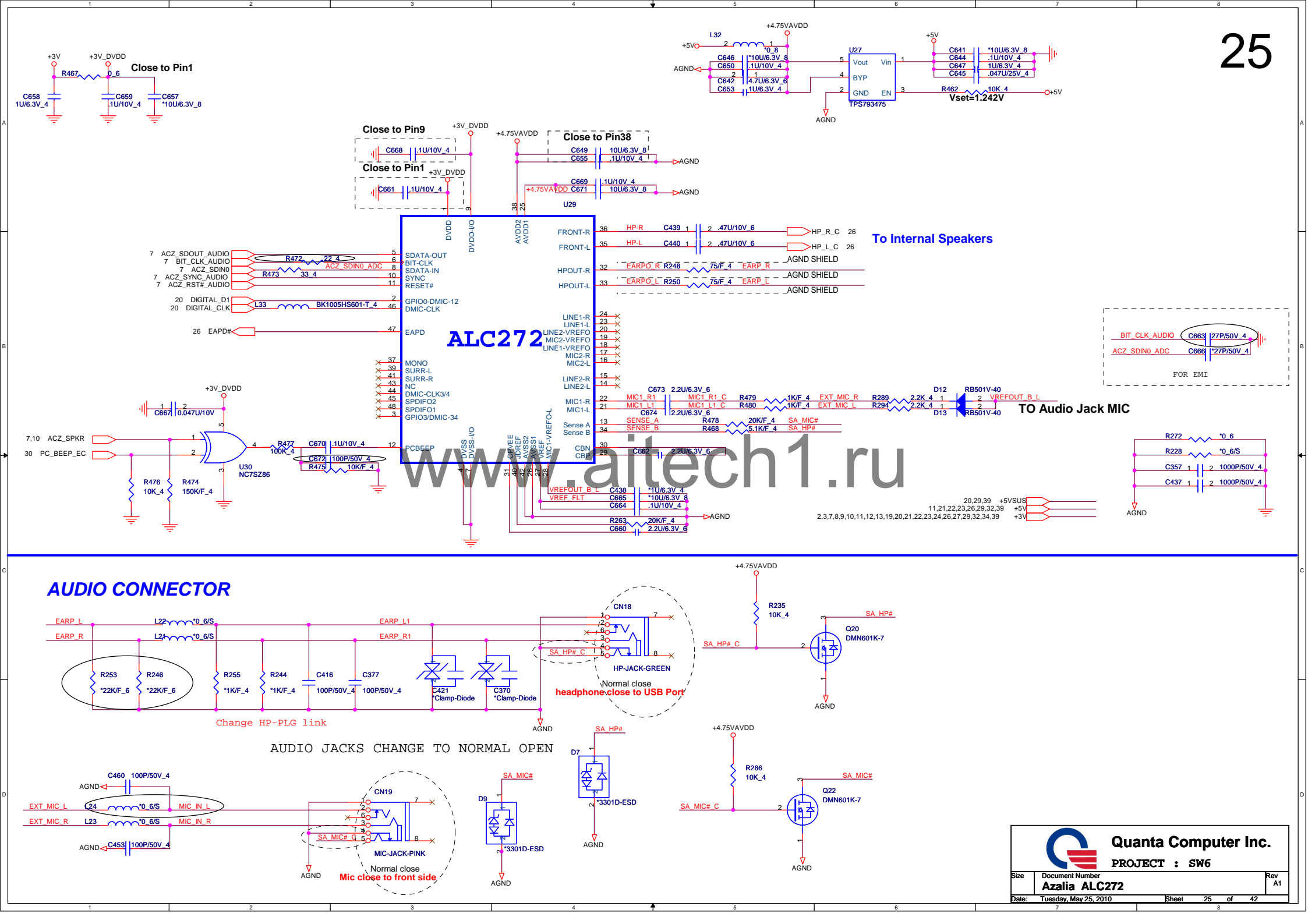


SATA ODD CONNECTOR

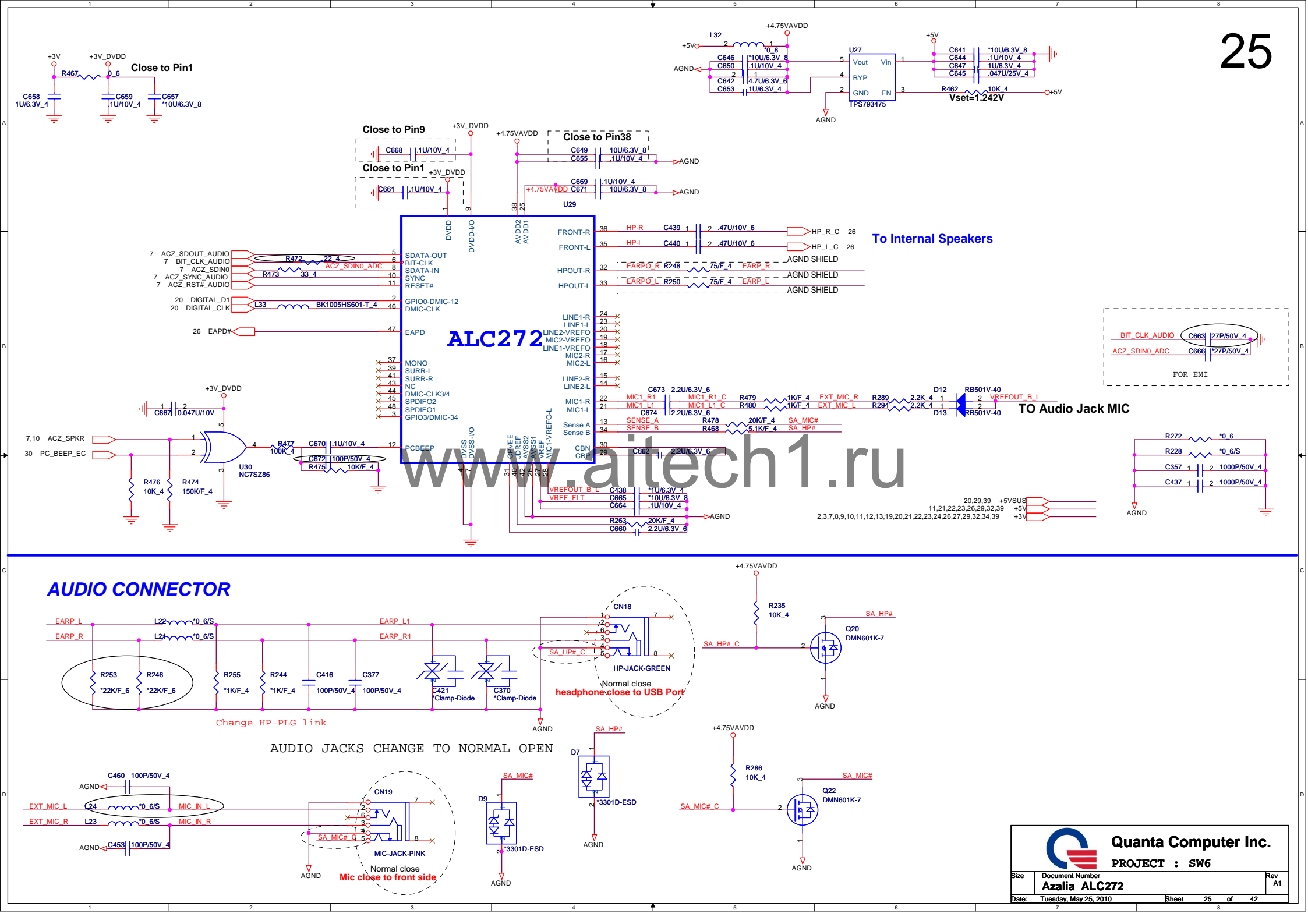


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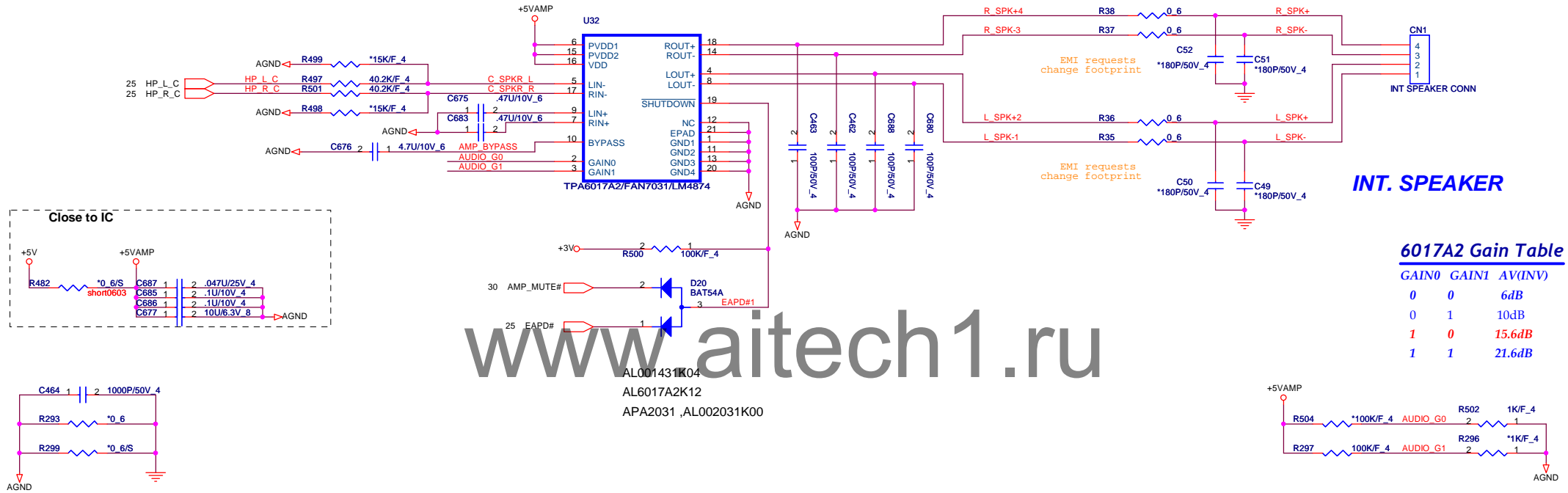
20,33,35,39 +15VALV
11,21,22,25,26,29,32,39 +5V
2,3,7,8,9,10,11,12,13,19,20,21,22,24,25,26,27,29,32,34,39 +3V

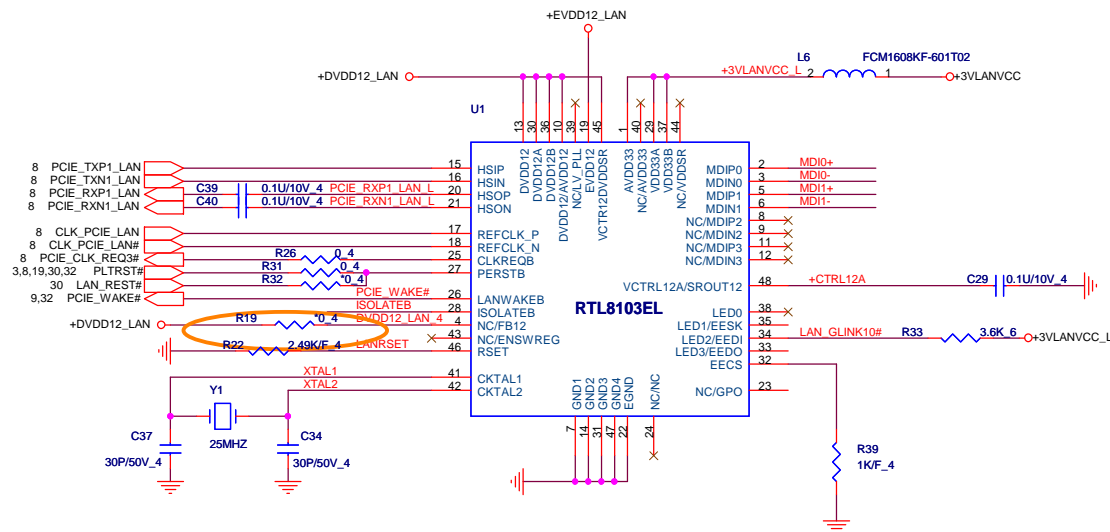


25

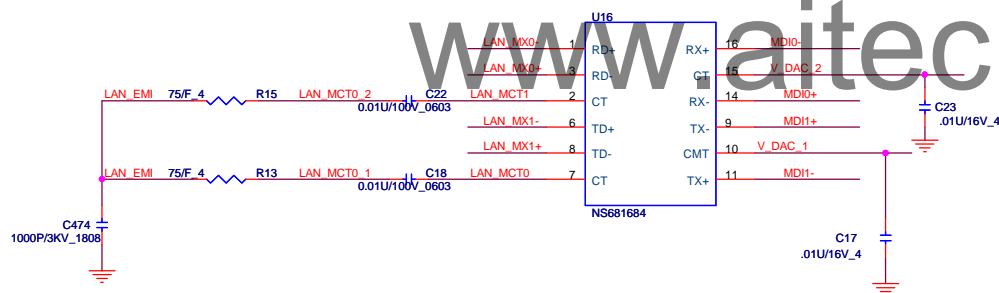


AUDIO AMPLIFIER

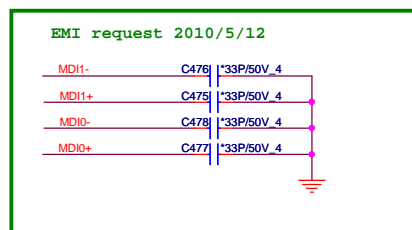




Transformer for 10/100

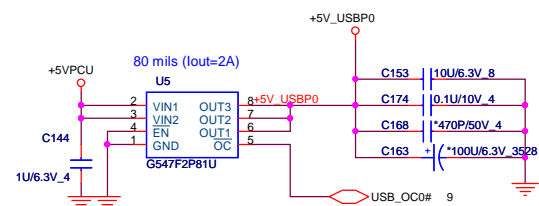


Lan Con.



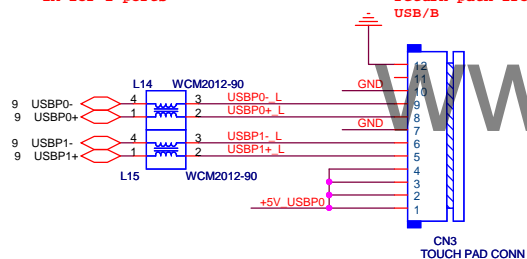
20,30,33,34,35,36,38,39,40,41

+5VPCU

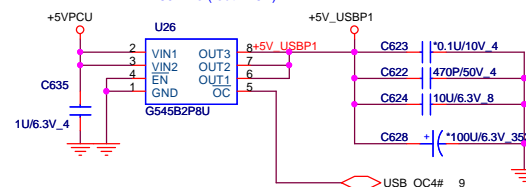
Right SIDE USBX2

USB I/O board Conn
2A for 2 ports

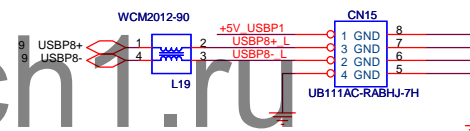
PIN12 is for ESD
return path from
USB/B

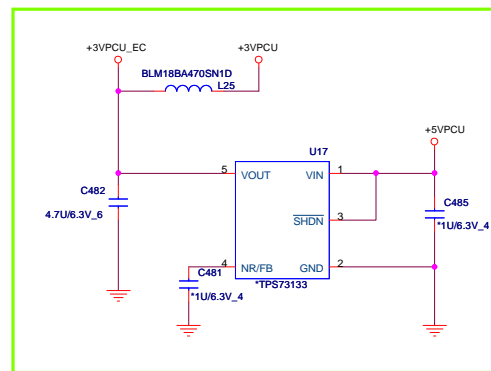
**LEFT SIDE USBX1**

60 mils (Iout=1.5A)

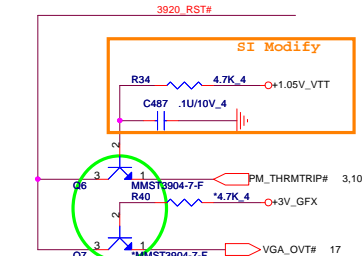
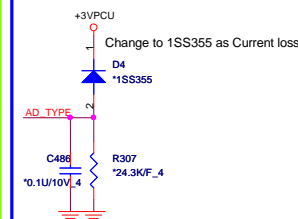


1.5A



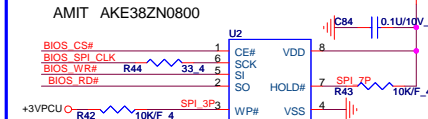


thermal shutdown circuit

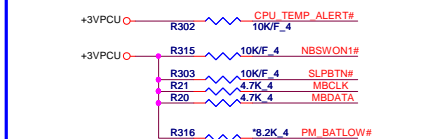


512K byte SPI EC ROM

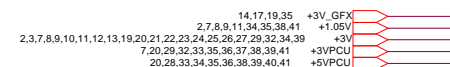
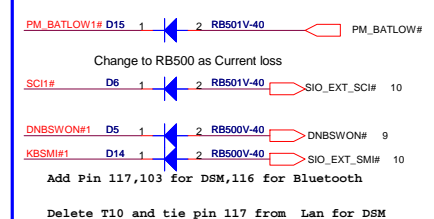
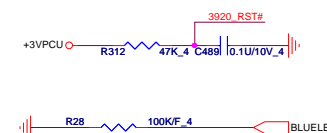
Socket: DG008000031
MXIC AKE3KZP0001
WINBOND AKE37ZN0N00
AMIT AKE38ZN0800

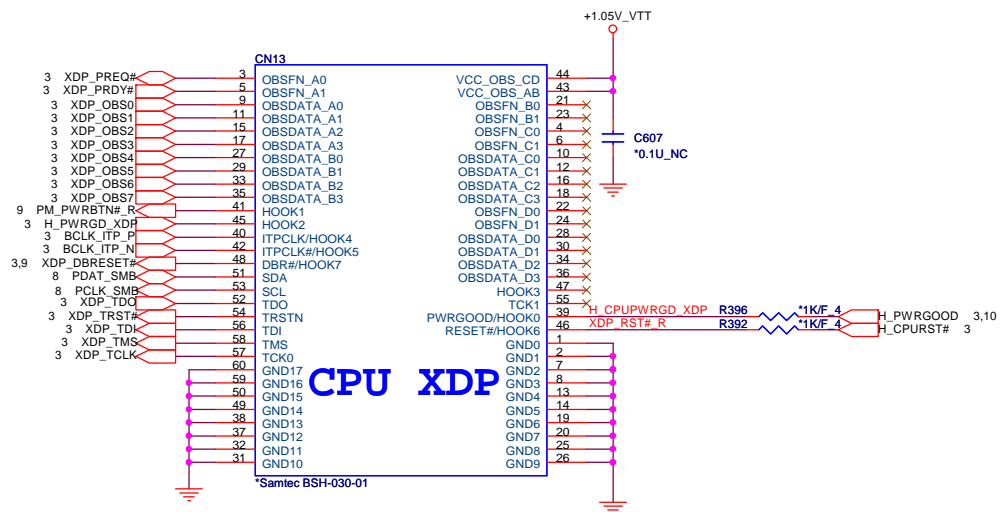


ID	Ra	Rb
120W	10K	N/A
65W/90W	N/A	10K



High: LG
Low : CB

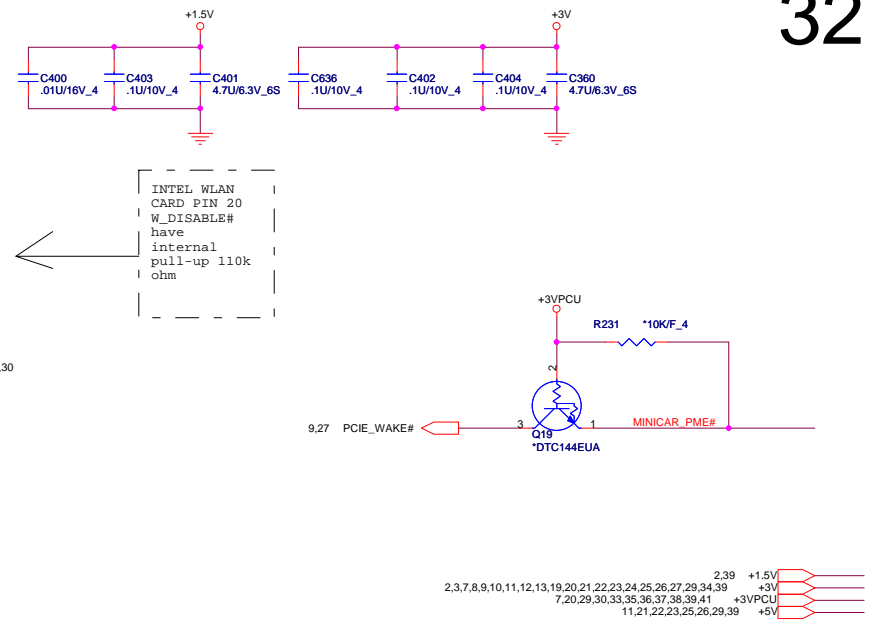




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3,5,10,11,30,34,40,41 +1.05V_VTT

32



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+3VPCU

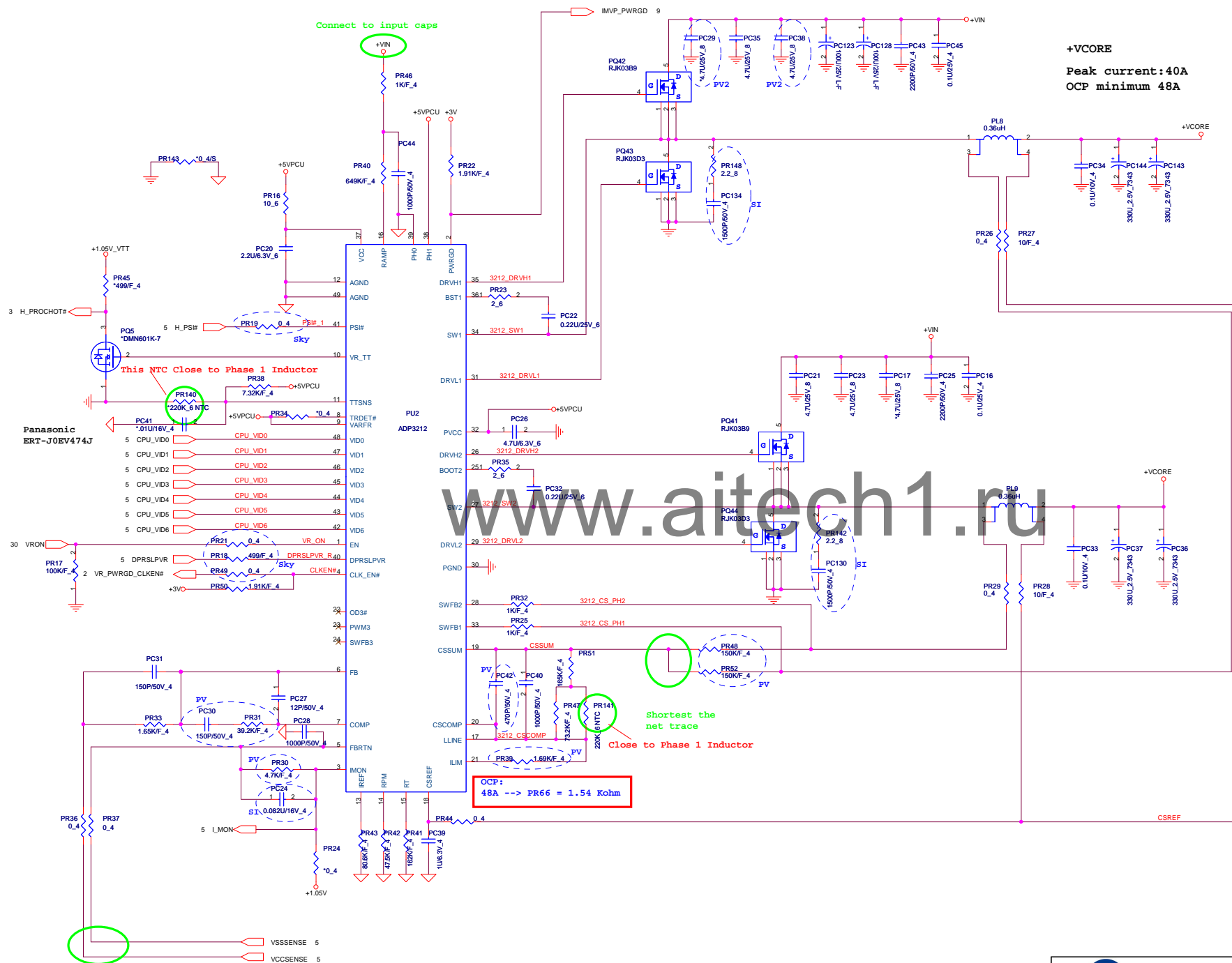
JP6
SHORT PAD

1. ru

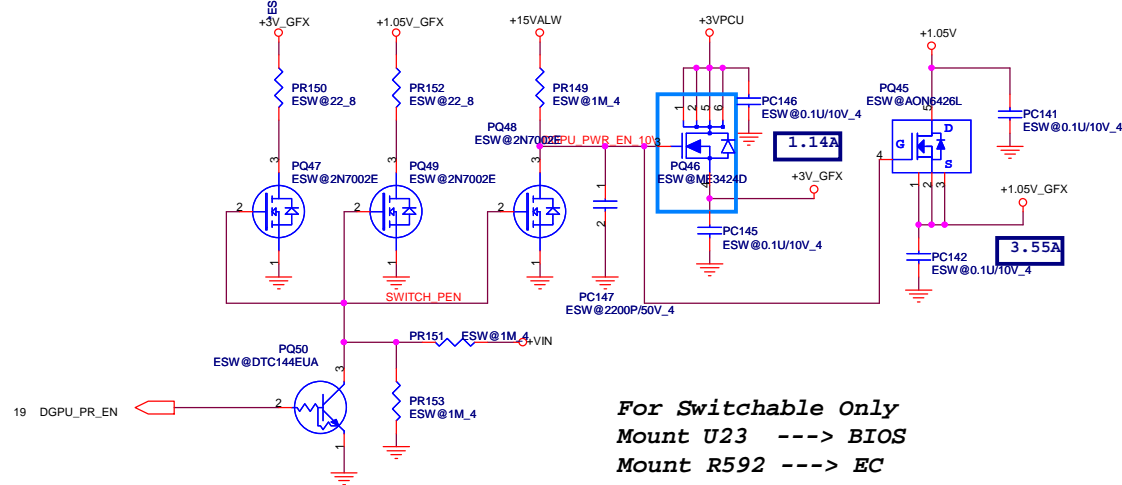
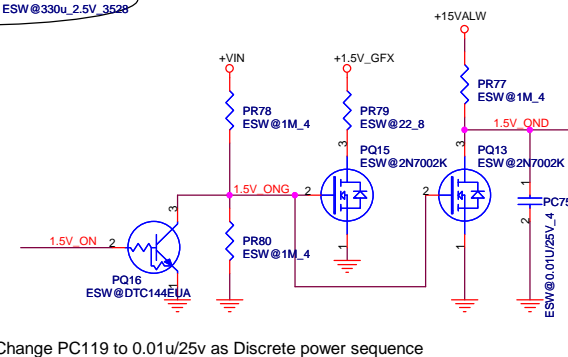
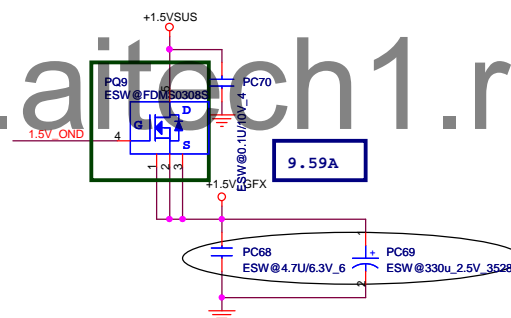
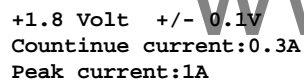
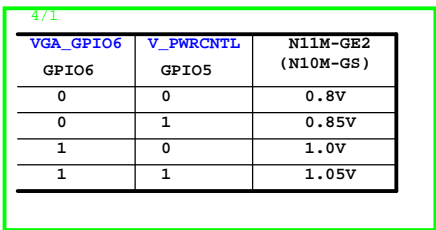


Size	Document Number +5V/+3V (RT8206B)	Rev A
Date:	Tuesday, May 25, 2010	Sheet 33 of 42

+VCORE
Peak current:40A
OCP minimum 48A

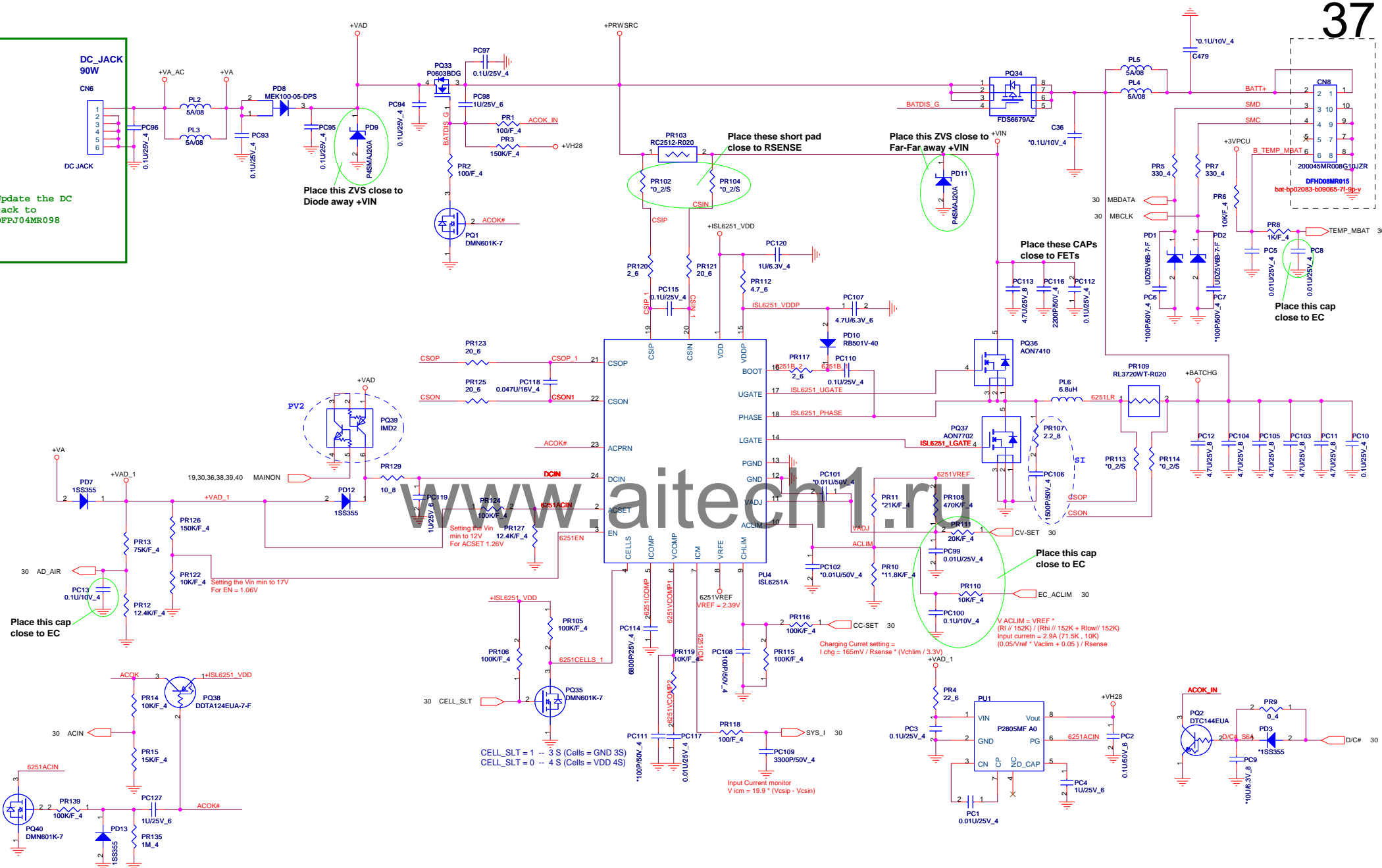


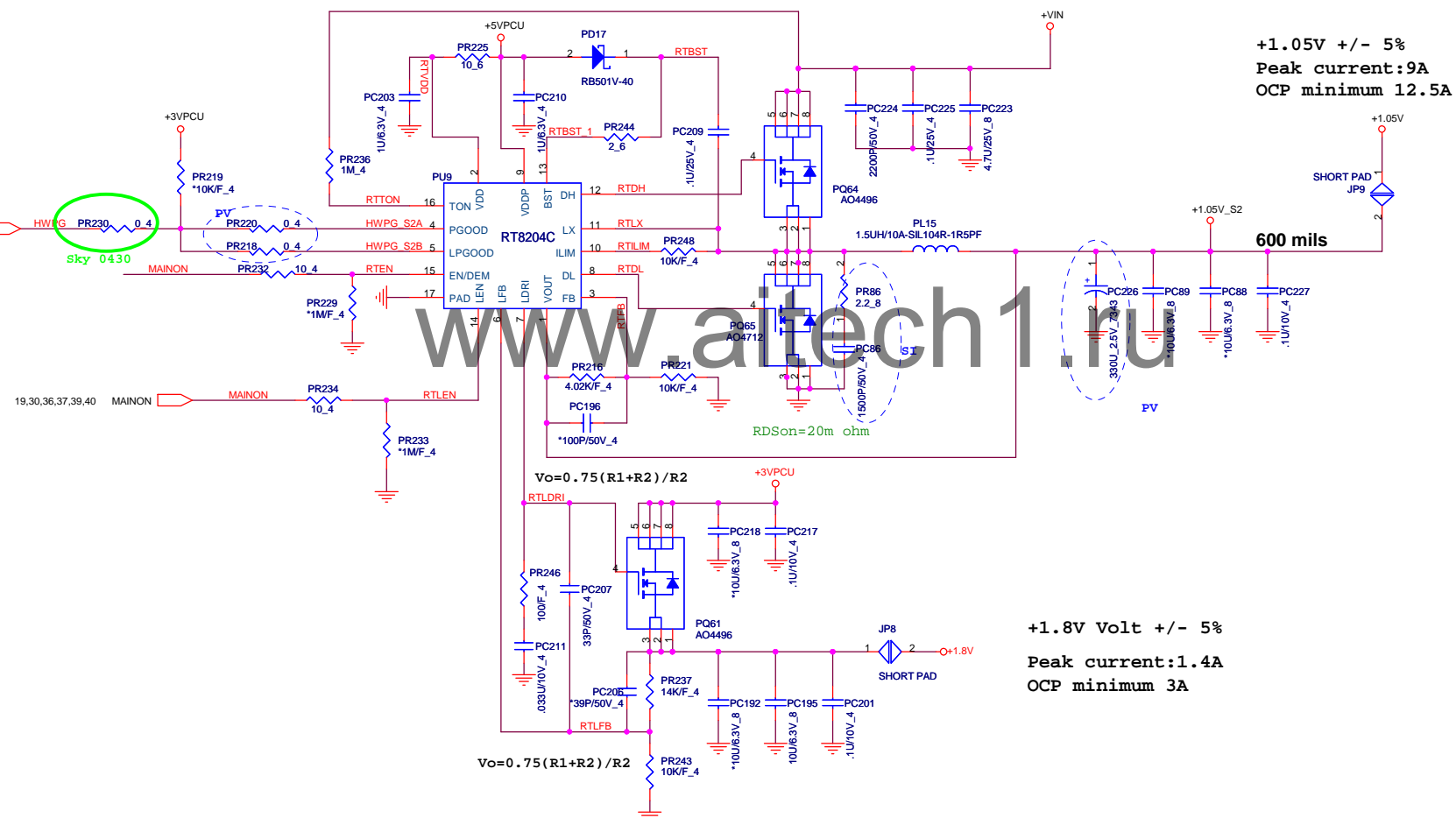
35



```
For Switchable Only
Mount U23 ---> BIOS
Mount R592 ---> EC
```

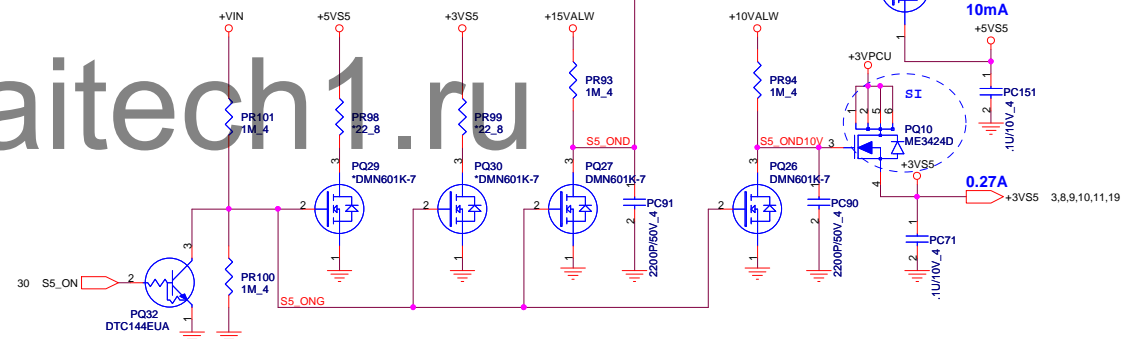
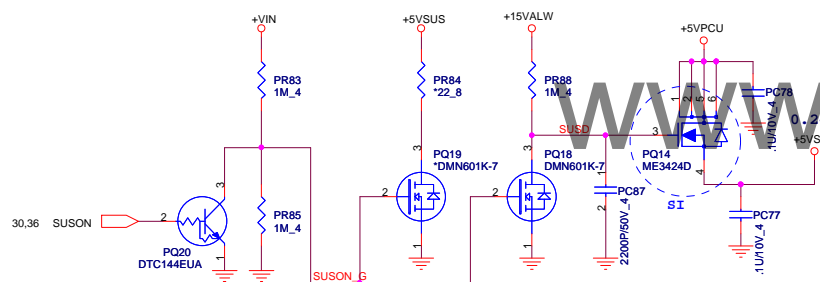
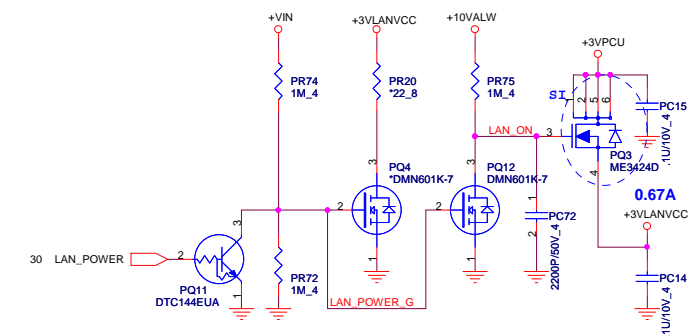
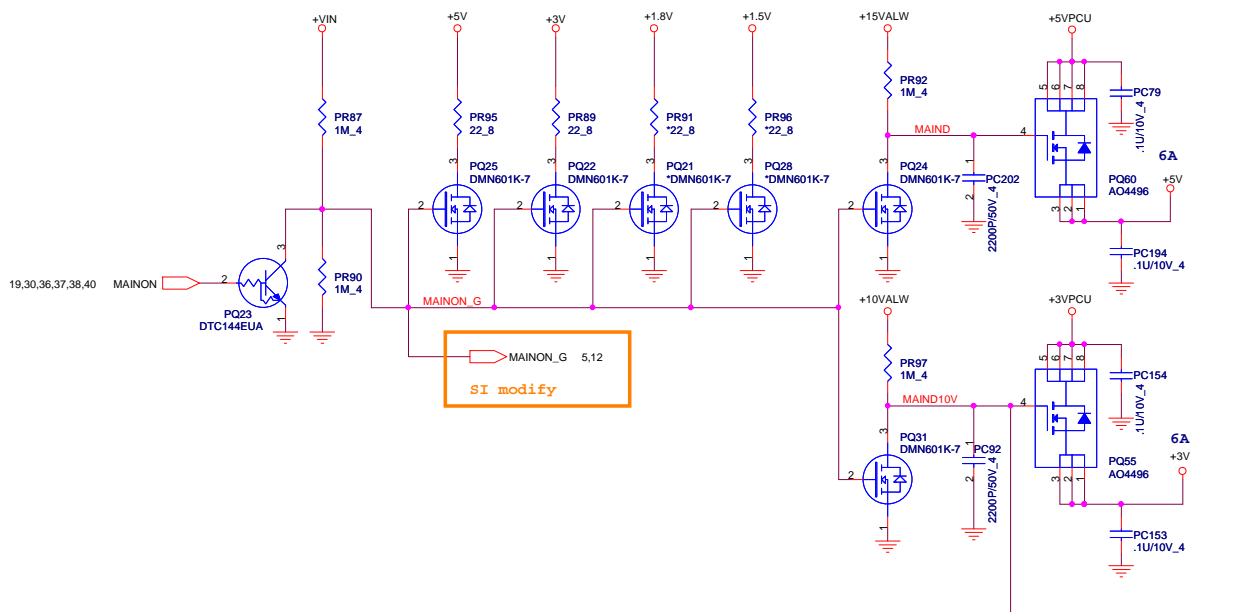




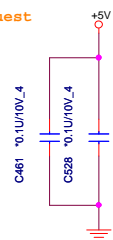


SI modify

MAIND 5



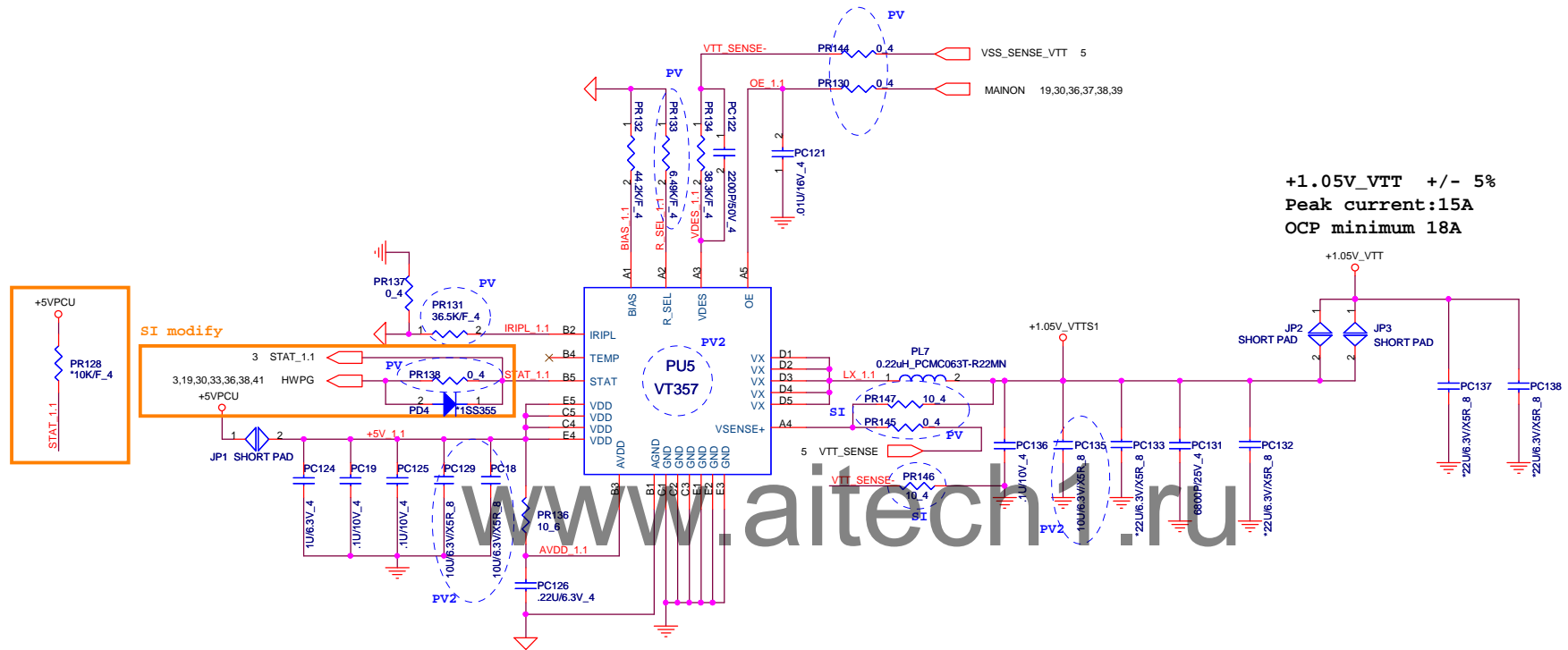
EMI request

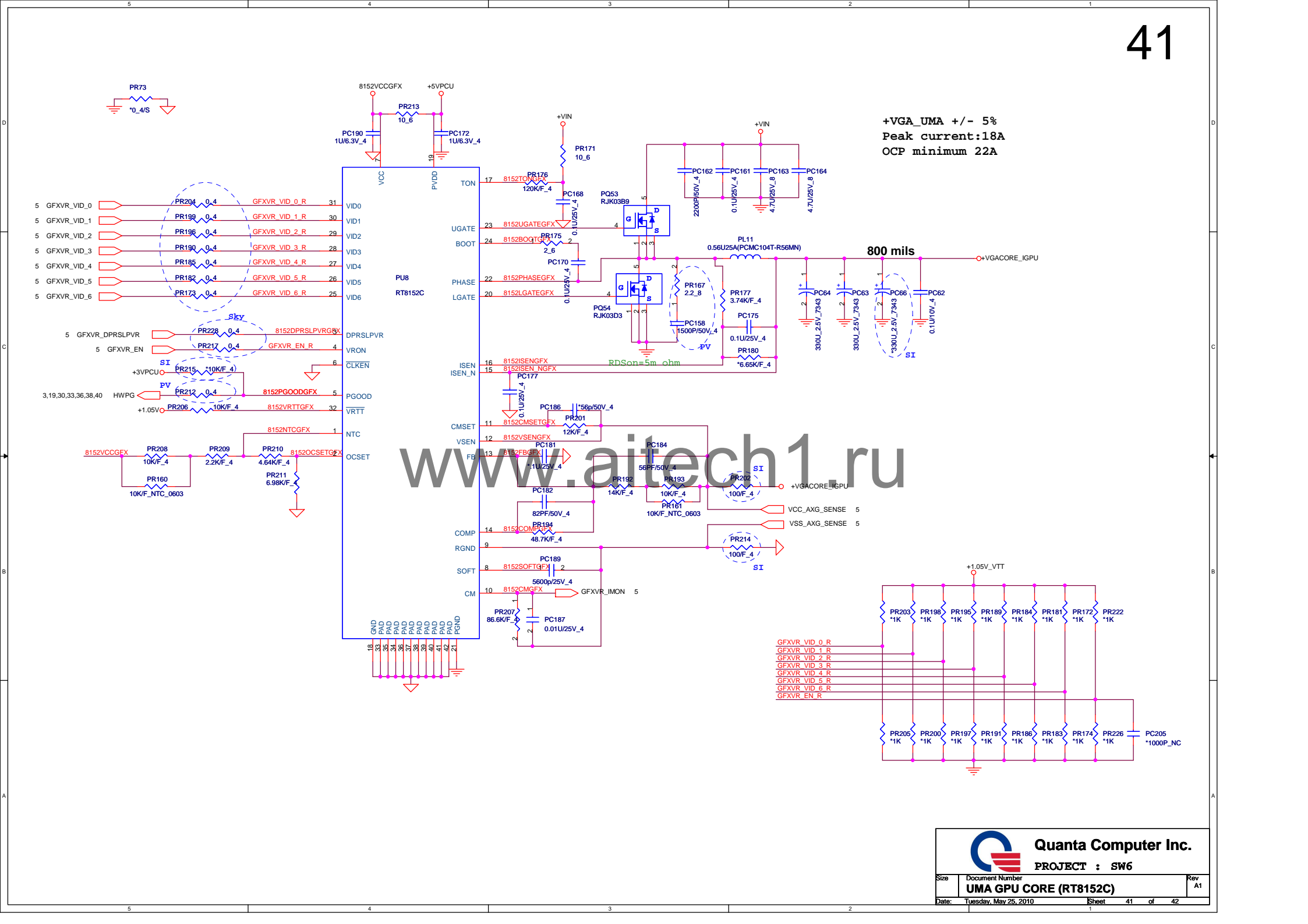


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PROJECT : SW6

Size	Document Number	Rev
	DISCHARGE/3VS5/5VS5/LAN	A1

Date: Tuesday, May 25, 2010 Sheet 39 of 42





ITEM	DATE	PAGE	DESCRIPTION
1	5/8	2	AS EMI request, add C51 and C44 on page 2 for 27MHZ GPU CLOCK
2	5/8	24	AS EMI request, Change R538 from 0_S to 33ohm and move C786 to net SD_CLK and change value to 27p
3	5/8	27	AS EMI request, Add C580/C583/C585/C588 on the MDI interface
4	5/10	37	Modify the battery connector CN16 PCB footprint for DFHD08MR015
5	5/10	38	Change the PC237 from CC7390JMZ02 to be CH733RY8802 for height limited
6	5/10	35	Delete the JP11 and JP12 of VGA core
7	5/10	2	Update the Hole footprint and dimension
8	5/10	3	Change R179 from 0ohm short pad to 0ohm
9	5/11	28	Swap L28 and L31 pin connection for USB+/- trace routing
10	5/11	2	Add Hole16
11	5/11	41	Delete JP15 and JP16 for the power +VGACORE_IGPU
12	5/14	20	Delete U1,C18,R218,R18,R20,R17 and C13;Add D23,R350,C19,C22 and C390 for +3.6V_CAM
13	5/14	27	Swap LAN_MX1+/- with LANMX0+/-, and swap MDI0+/- and MID1+/- for EMI routing
14	5/14	28	Add R336 for the GND connection for M/B side and ESD side
15	5/14	2	Chagne hole18(H18) to AGND for Audio
16	5/18	29	Add LED5 and R355 for the WLAN+BT LED function
17	5/18	30	Delete Q17
18	5/18	32	Add R362 for Blue tooth LED function
19	5/18	20	Delete R19, Swap L4
20	5/18	2	Change H19,H5,H7 and H16 not connect to GND
21	5/18	19	Add R336 for the DGPU_PWR_EN function
22	5/18	28	Delete C344,C350,C426,C343 for Cost issue, Add C53 for +5V_USBP0, Change C46 from 0402 to 0805 type
23	5/20	2	Add Hole 22 for Fan module
24	5/20	27	Add R351 for U22.4 pin for DVDD12_LAN
25	5/21	28	Swap Pin defination for CN11
26	5/21	8/15	Change C10361 and C10437 from CH6102M9900(10u_0603) to CH61001ME96(10U_0805)
27	5/21	7	Delete C499 and C500 for LVDS clock signal
28	5/21	All	Back annotate from layout
29	5/24	13	Change R47 from 0_short to 0_0402
30	5/24	2	Update H2 to h-c256d118p2

ALWAYS

S5

S3

S0

